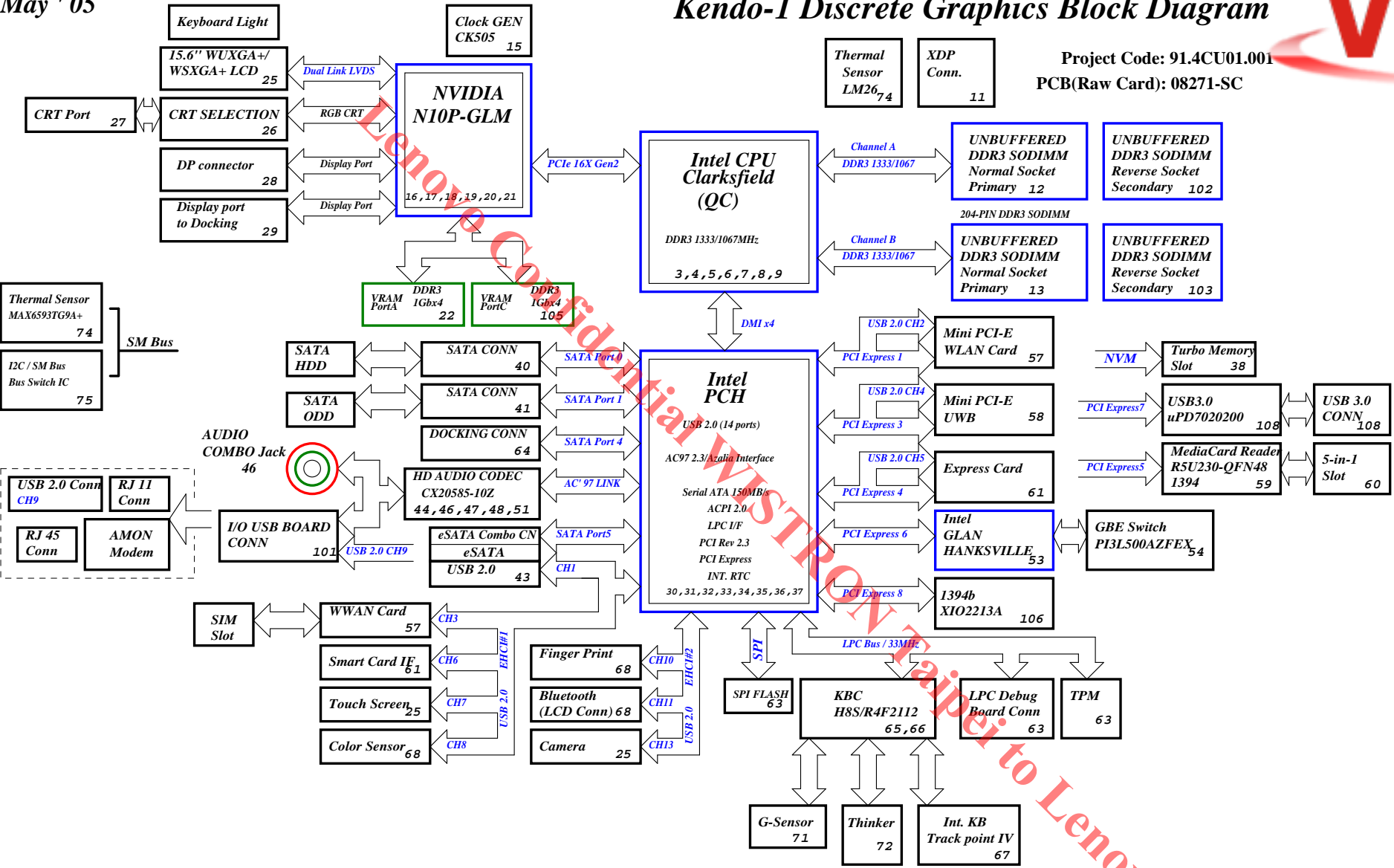


Kendo-1 Discrete Graphics Block Diagram



PCB Layout Stackup

1: TC
2: Signal 1
3: GND
4: Signal 2
5: VCC
6: GND
7: Signal 3
8: GND
9: Signal 4
10: BOTTOM

Battery Charger/Selector

BQ24741RHDR 78	
INPUTS	OUTPUTS
DOCK_PWR20_F	M-BAT-PWR
	S-BAT-PWR

System DC/DC

TPS51222RTV 82	
VINT20	VCC5M
	VCC3M

CPU DC/DC

ADP3212MNR2G 83	
VINT20	VCCPUCORE

GFXCORE\_D

ADP3211 85	
VINT20	VCCGFXCORE

VCCIR5VIDEO

MAX8792 91	
VCC5M	VCCIR5VIDEO

VCCIR5A/DDR3\_REF

VCCOR75B 89	
VCC5M	DDR3_VREF
	VCCOR75B
	VCCIR5A

VCCIR8B

BD3551 92	
VCC3M	VCCIR8B

VCCIR1B\_VTT

VCCIR05LAN 87	
VINT20	VCCIR1B_VTT
VCC5M	VCCIR05LAN

<Variant Name>

緯創資通 Wistron Corporation	
21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Block Diagram	
Title	Rev
Size	Custom
Document Number	SC
Date: Tuesday, May 05, 2009	Sheet 1 of 109





Need to refer Intel Design Guide to place CPU and route wire.

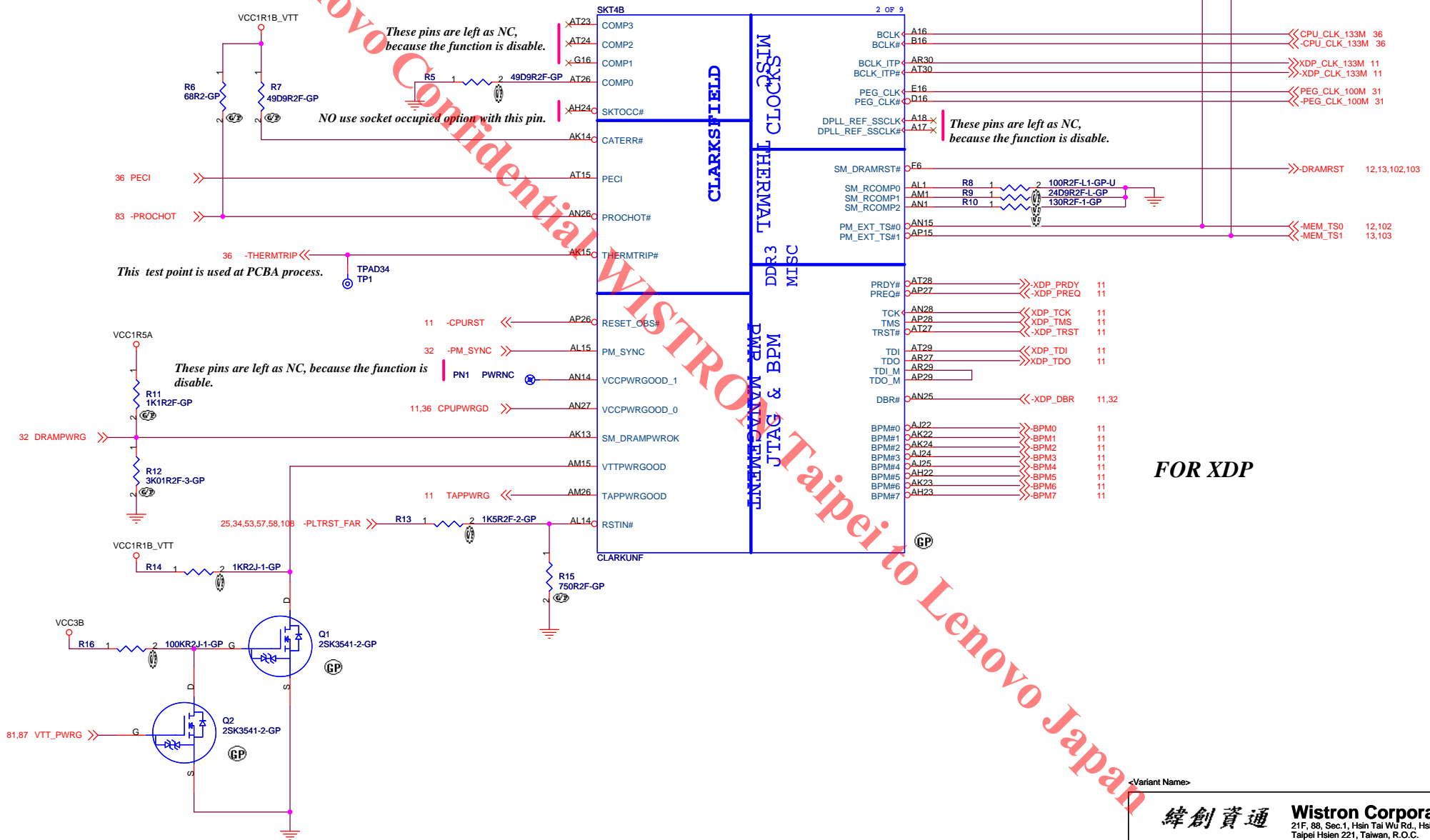
No use Catastrophic Error  
Option with this pin.  
Thermal Protection of CPU  
at High Temp is controlled by LM26.

These pins are left as NC,  
because the function is disable.

NO use socket occupied option with this pin.

36 PECCI  
83 -PROCHOT  
36 -THERMTRIP  
This test point is used at PCBA process.

These pins are left as NC, because the function is  
disable.



FOR XDP

<Variant Name>

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title		CPU (27):CLK/MISC/JTAG	
Size	Document Number	Rev	
A3		Kendo-1 WS SC	
Date: Tuesday, May 05, 2009		Sheet 4 of 109	

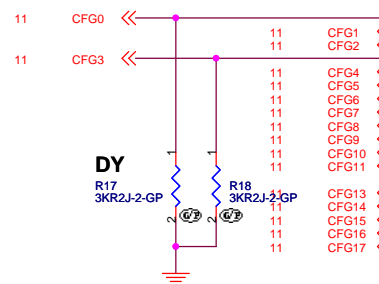
Lenovo Confidential Wistron to Lenovo Japan

12,102 M\_A\_B50 <<< AC3 SA\_B50  
12,102 M\_A\_BS1 <<< AB2 SA\_BS1  
12,102 M\_A\_BS2 <<< U7 SA\_BS2  
  
12,102 -M\_A\_CAS <<< AE1C SA\_CAS#  
12,102 -M\_A\_RAS <<< AB2C SA\_RAS#  
12,102 -M\_A\_WE <<< AE2C SA\_WE#

13,103 M\_B\_B50 <<< AB1 SB\_B50  
13,103 M\_B\_BS1 <<< W5 SB\_BS1  
13,103 M\_B\_BS2 <<< R7 SB\_BS2  
  
13,103 -M\_B\_CAS <<< AC3C SB\_CAS#  
13,103 -M\_B\_RAS <<< AC2C SB\_RAS#  
13,103 -M\_B\_WE <<< AC2C SB\_WE#

SB\_MAO0 U5 M\_B\_A0  
SB\_MAO1 V2 M\_B\_A1  
SB\_MAO2 T5 M\_B\_A2  
SB\_MAO3 V3 M\_B\_A3  
SB\_MAO4 R1 M\_B\_A4  
SB\_MAO5 T8 M\_B\_A5  
SB\_MAO6 R2 M\_B\_A6  
SB\_MAO7 R6 M\_B\_A7  
SB\_MAO8 R4 M\_B\_A8  
SB\_MAO9 R5 M\_B\_A9  
SB\_MAO10 AB5 M\_B\_A10  
SB\_MAO11 P3 M\_B\_A11  
SB\_MAO12 R3 M\_B\_A12  
SB\_MAO13 AF7 M\_B\_A13  
SB\_MAO14 P5 M\_B\_A14  
SB\_MAO15 N1 M\_B\_A15

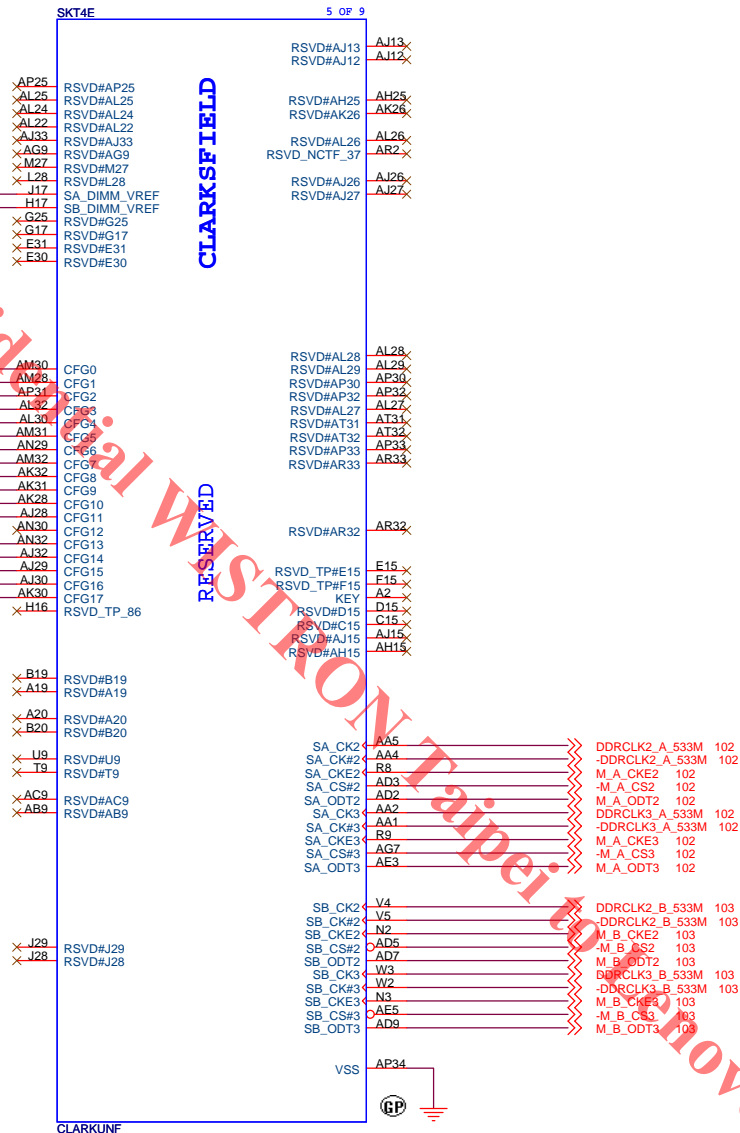
Need to confirm about Internall Pullup



CFG[1:0] => 11 : 1x16 PEG  
10 : 2 x 8 PEG

CFG3 => 1 : Normal Operation  
0 : Lane Numbers Reversed.

CFG[12]:This pin is left as NC,  
because the GFX function is disabled.



4-DIMM SUPPORT

<Variant Name>

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

CPU(4/7): CFG/RSVD

Size

Document Number

Kendo-1 WS

Rev

SC

Date: Tuesday, May 05, 2009

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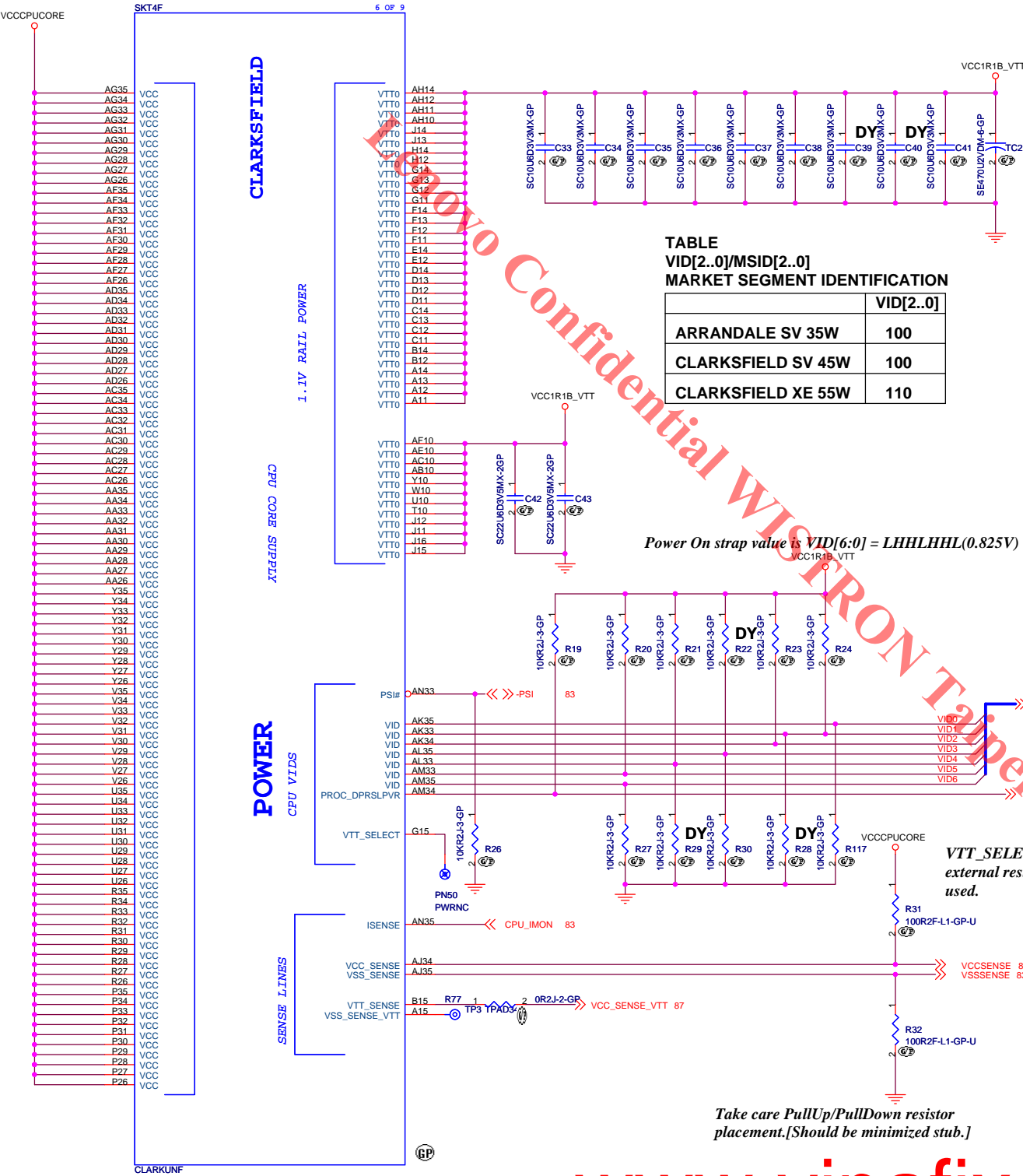


TABLE  
VID[2..0]/MSID[2..0]  
MARKET SEGMENT IDENTIFICATION

	VID[2..0]
ARRANDALE SV 35W	100
CLARKSFIELD SV 45W	100
CLARKSFIELD XE 55W	110

TABLE  
VID[5..3]/CSC[2..0]  
CURRENT SENSE CONFIGURATION

IMAX	VID[5..3]
DISABLE	000
20A	001
30A	010
40A	011
50A	100
60A	101
70A	110
80A	111

ARD SV Iccmax=48A  
CF SV Iccmax=52A  
CF XE Iccmax=65A

Power On strap value is VID[6:0] = LHH LHH L(0.825V)

VTT\_SELECT [G15]: Because VCC1R05B\_VTT is fixed voltage with external resistor in DC/DC circuits, the VTT\_SELECT signal is not used.

Take care PullUp/PullDown resistor placement.[Should be minimized stub.]

緯創資通

Wistron Corporation

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Title

CPU(5/7): PWR

Size

Custom

Document Number

Kendo-1 WS

Date

Tuesday, May 05, 2009

Sheet

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109

Rev

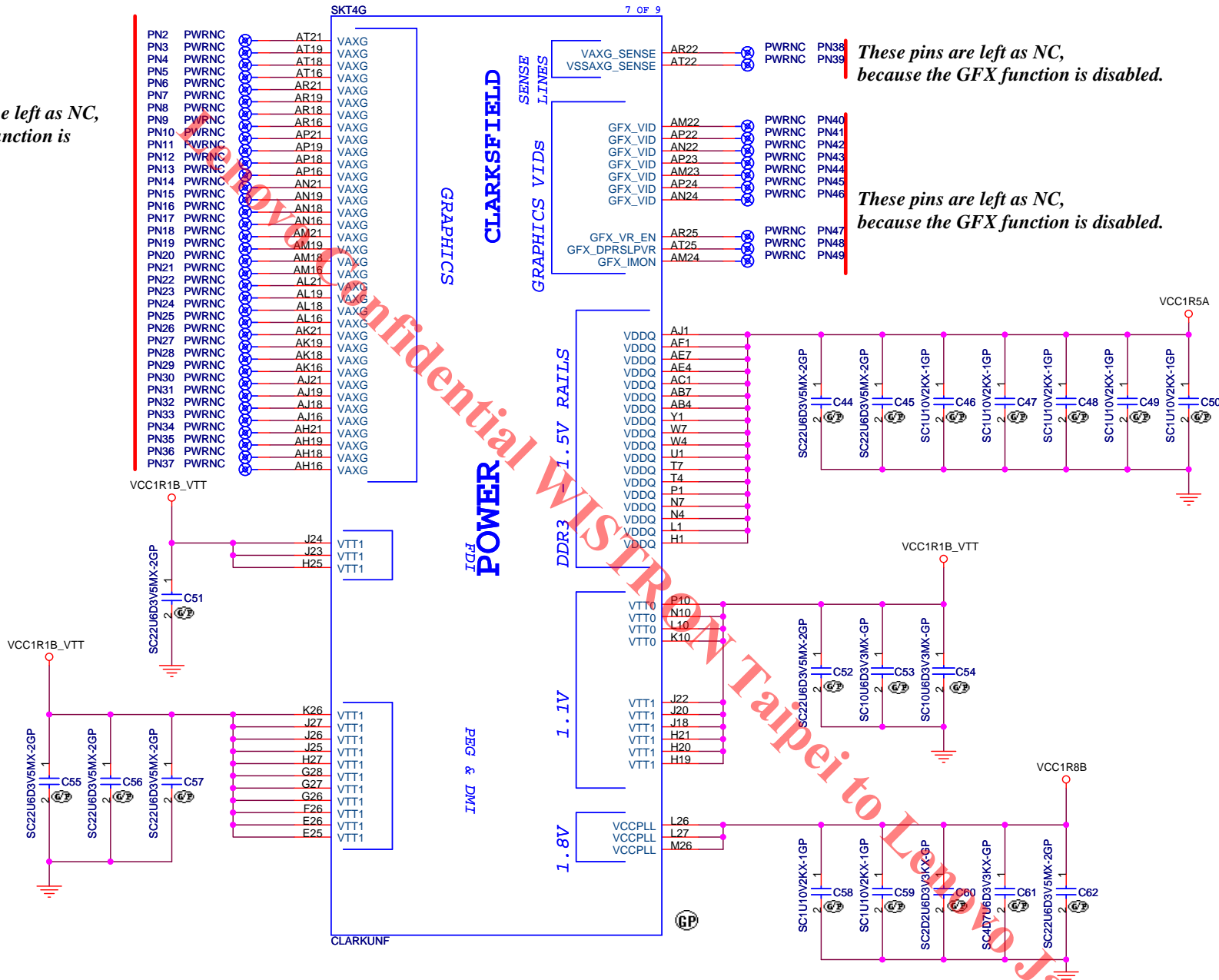
SC



*These pins should be left as NC, because the GFX function is disabled.*

*These pins are left as NC,  
because the GFX function is disabled.*

*These pins are left as NC,  
because the GFX function is disabled.*



<Variant Name>

緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

Title

**CPU(6/7): GFX/PWR**

Size

Document Number

## Kendo-1 WS

Rev

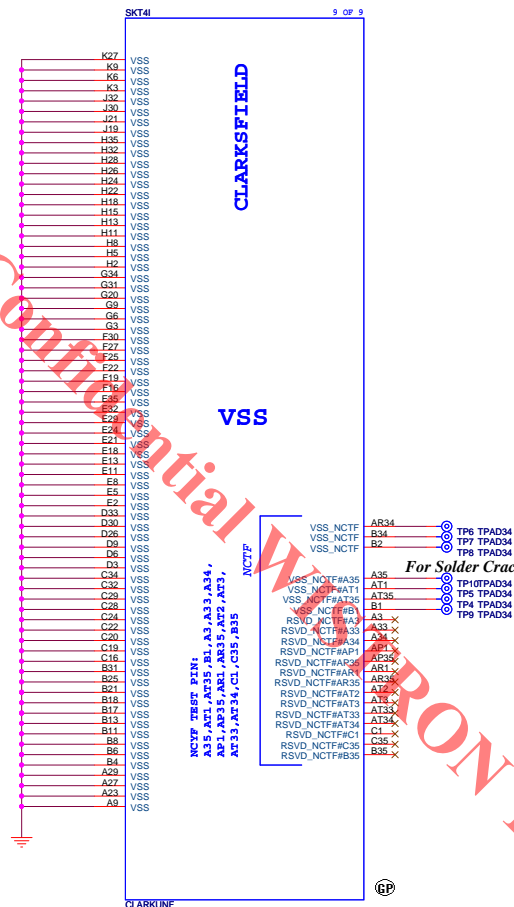
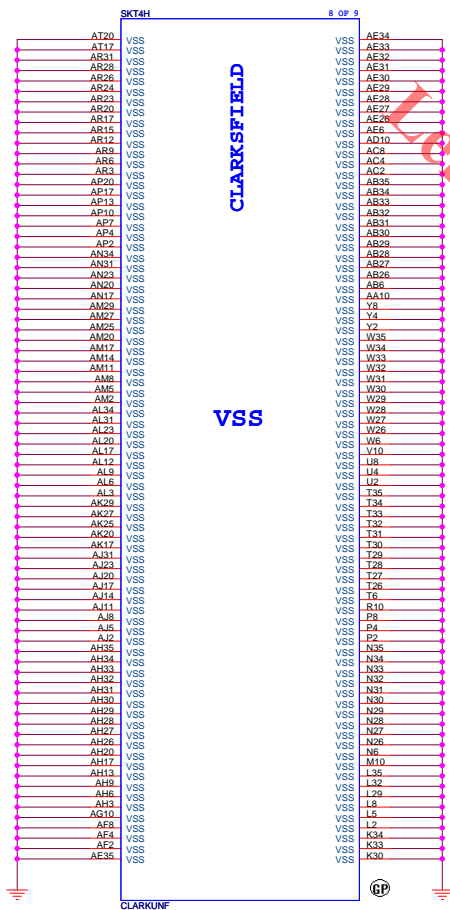
So

Date: Tuesday, May 05, 2009

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of 10





*For Solder Crack Detection.*

**For Solder Cracks**

VSS_NCTF#A35	A35	TP10TPAD34
VSS_NCTF#AT1	AT1	TP5 TPAD34
VSS_NCTF#AT35	AT35	TP4 TPAD34
VSS_NCTF#B3	B3	TP9 TPAD34
RSVD_NCTF#A9	A9	
RSVD_NCTF#A33	A33	
RSVD_NCTF#A34	A34	
RSVD_NCTF#AP1	AP1	
RSVD_NCTF#AP35	AP35	
RSVD_NCTF#AR1	AR1	
RSVD_NCTF#AR35	AR35	
RSVD_NCTF#AT1	AT1	
RSVD_NCTF#AT3	AT3	
RSVD_NCTF#AT33	AT33	
RSVD_NCTF#AT34	AT34	
RSVD_NCTF#C1	C1	
RSVD_NCTF#C35	C35	
	B35	

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<Variant Name>	
緯創資通 <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
BLANK	
Size A3	Document Number <b>Kendo-1 WS</b>
Date: Tuesday, May 05, 2009	Sheet 10 of 109
Rev SC	

XDP Present function does not use.

Use these jumper, if pin count is overflow.

I2C Interface does not use.

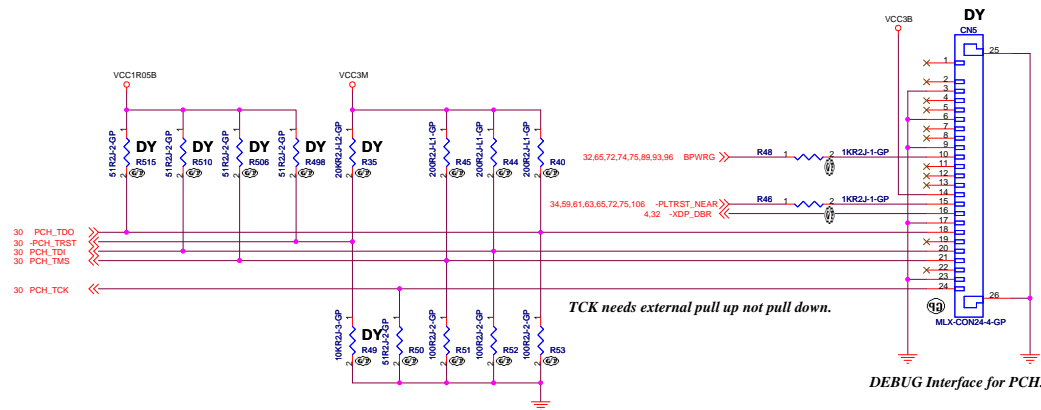
Short cut usage.

DEBUG Interface for Processor.

Short cut usage.

PCH PIN	REF DES	PCH ES1 JTAG		PCH ES2 JTAG		MP	
		ENABLE	DISABLE	ENABLE	DISABLE	ENABLE	DISABLE
TDO	R40	NO_ASM	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R53	NO_ASM	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R515	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TMS	R37	200	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R51	100	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R510	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TDI	R38	200	20K	200	NO_ASM	NO_ASM	NO_ASM
	R52	100	10K	100	NO_ASM	NO_ASM	NO_ASM
	R506	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TCK	R50	51	51	51	51	51	51
TRST#	R35	20K	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
	R49	10K	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
	R498	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM

LOGIC

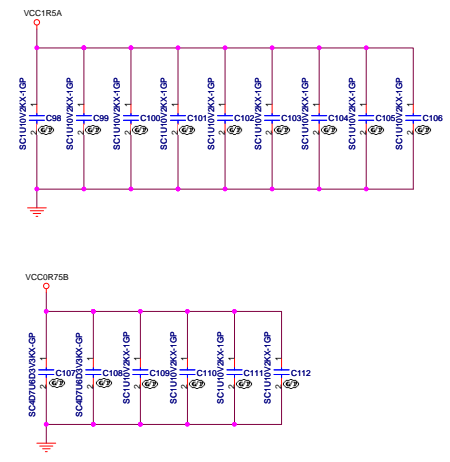
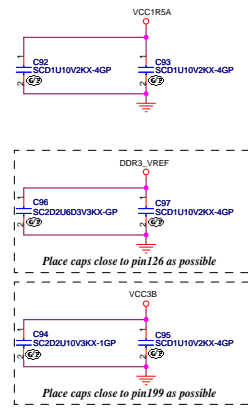
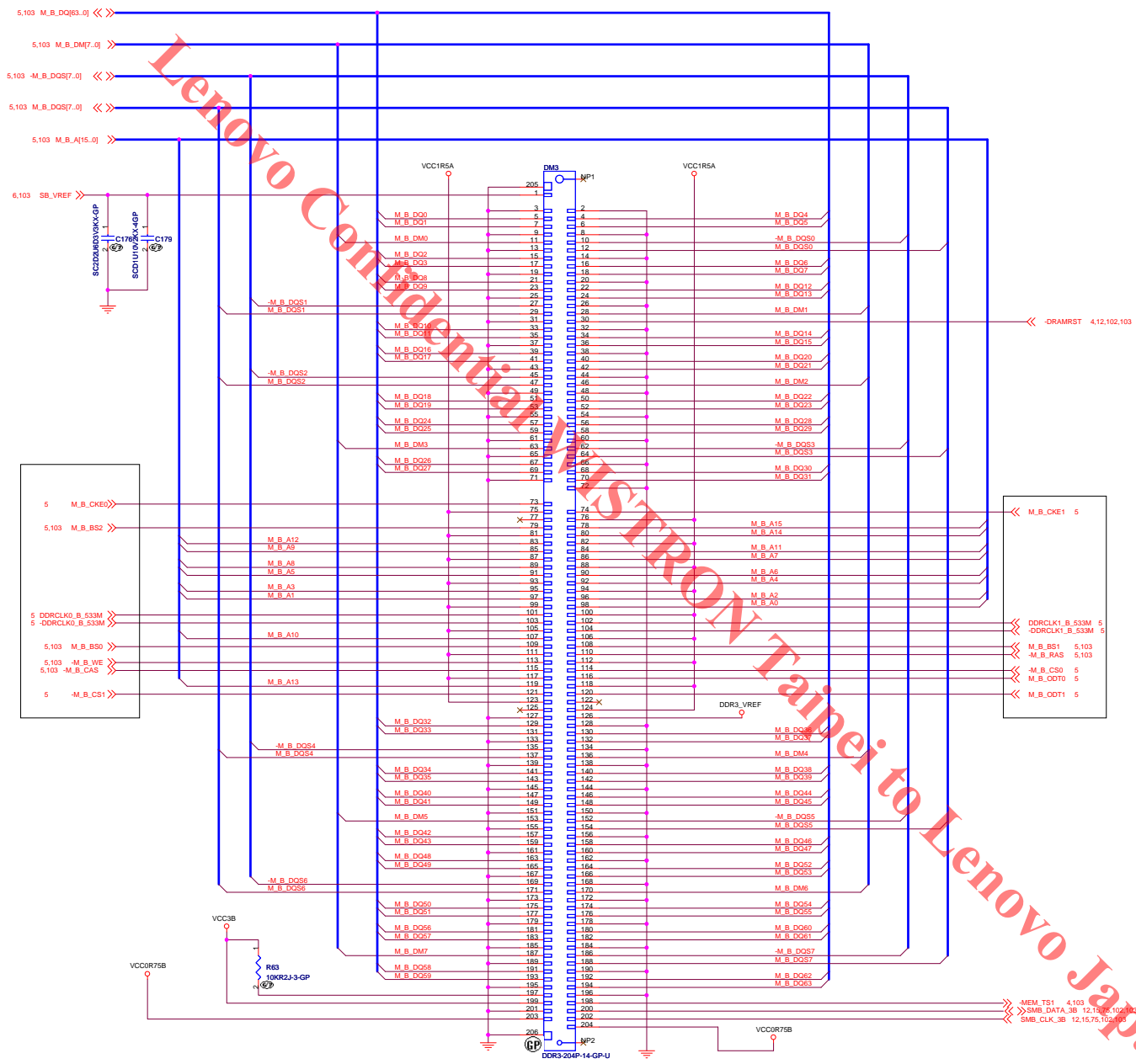


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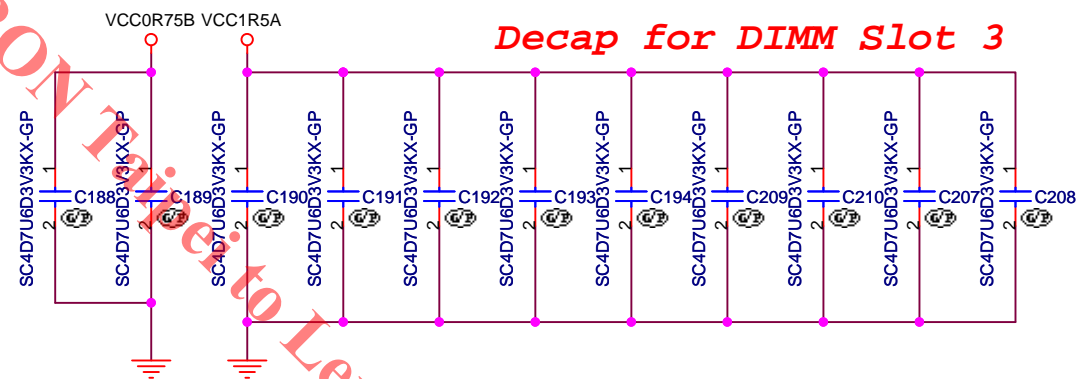
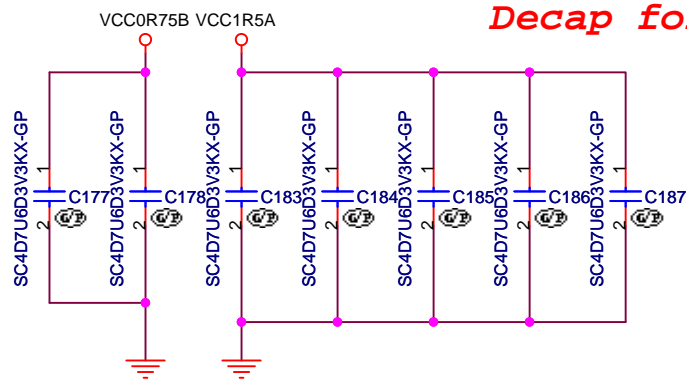
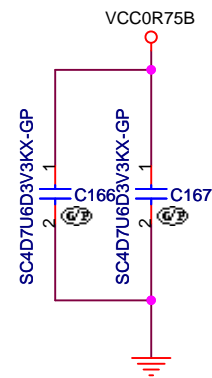
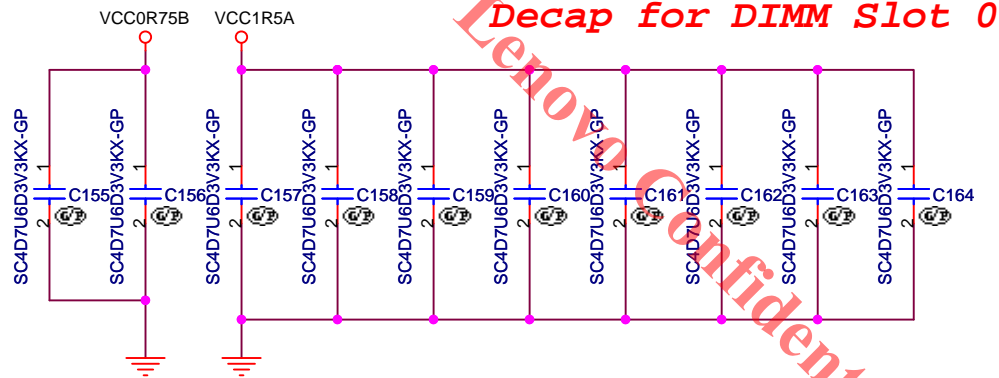
緯創資通 Wistron Corporation  
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XDP CONNECTOR			Rev
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<Variant Name>

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**Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

Title  
**DECAP For DIMMs**

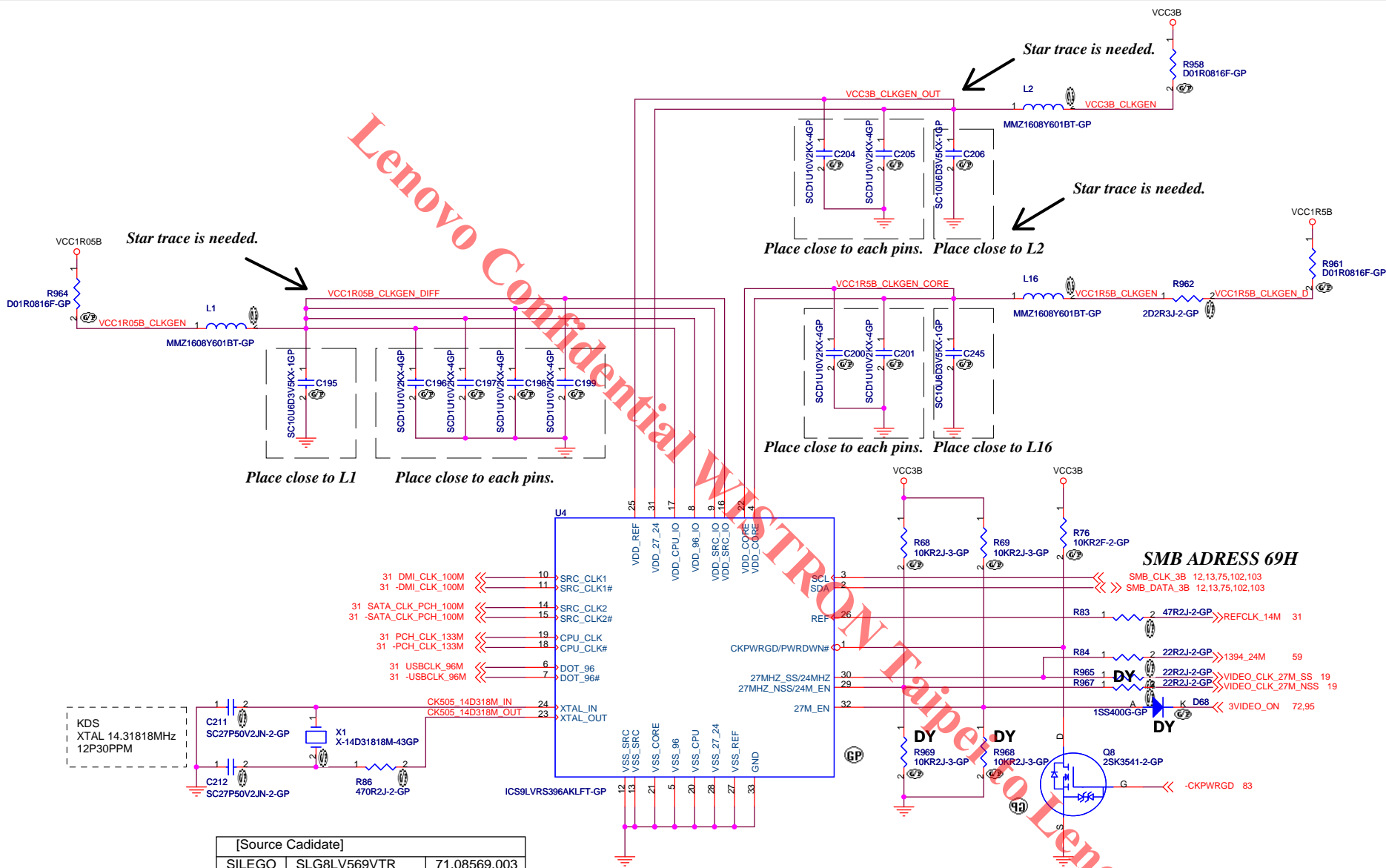
Size  
A4 Document Number

**Kendo-1 WS**

Rev  
**SC**

Date: Tuesday, May 05, 2009

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KDS Recommended Conditions:  
 Normal Frequency: 14.318180MHz.  
 Frequency Tolerance: +/- 30ppm.  
 Load Frequency: 12pF.  
 Effective Series Resistance: 50-ohm.  
 Effective Shunt Capacitance: 7pF.

IDT Recommended Conditions:  
 Normal Frequency: 14.318MHz.  
 Frequency Tolerance: +/- 30ppm.  
 Load Frequency: 20pF.  
 Effective Shunt Capacitance: 5pF.

Realtek Recommended Conditions:  
 Normal Frequency: 14.318MHz.  
 Frequency Tolerance: +/- 50ppm.  
 Load Frequency: 10 ~ 32 pF.  
 Effective Series Resistance: 40-ohm max.  
 Effective Shunt Capacitance: 7pF.

Silego Recommended Conditions:  
 Normal Frequency: 14.31818MHz.  
 Frequency Tolerance: +/- 30ppm.  
 Load Frequency: 20 pF.  
 Effective Shunt Capacitance: 5pF.

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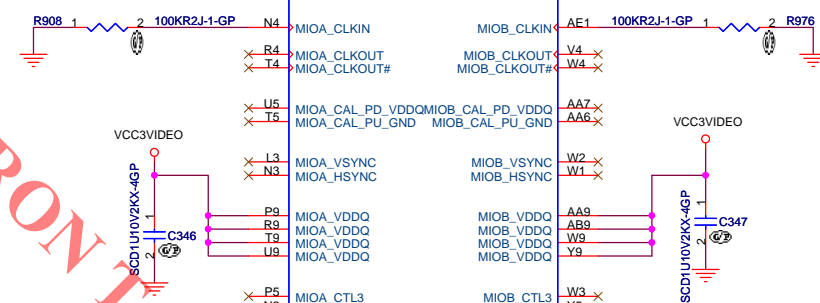




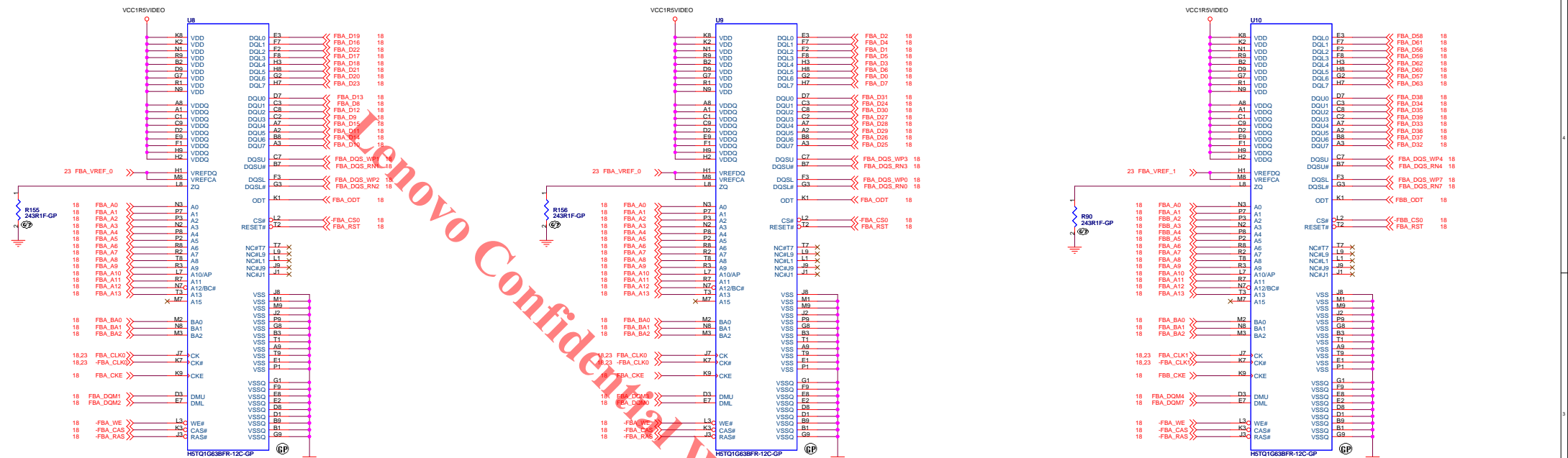




**MIOA\_D0-D9 and MIOB\_D0-D9 are optional debug port.**





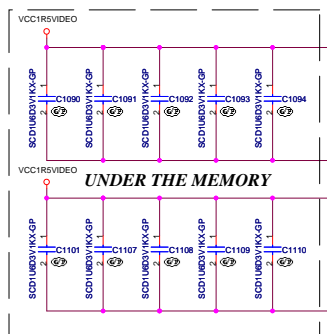


FB CMD mapping Mode A-N10x

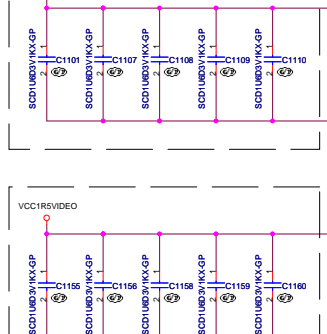
Need to Check the DDR3 VDD Current rating.

DDR3 Power consumption 1.5V@800MHz  
 Hynix :IDD4W=220.4mA  
 Samsung :IDD7=150.4mA (calculated)

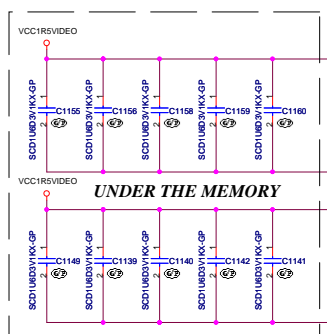
FOR U8



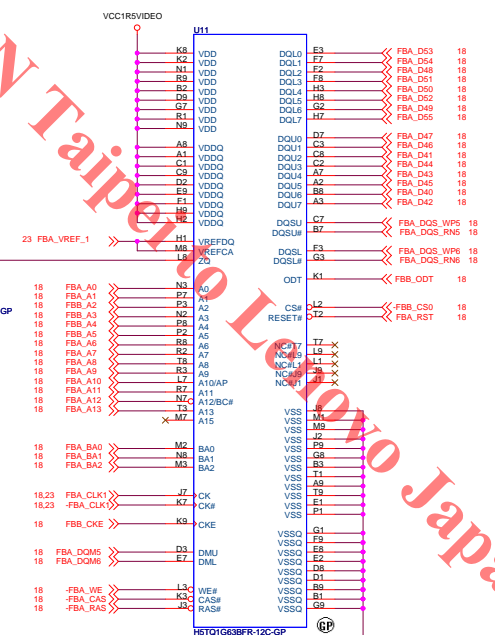
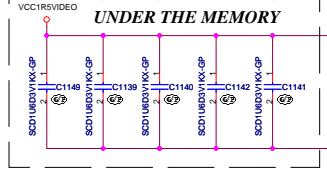
FOR U9



FOR U10



FOR U11

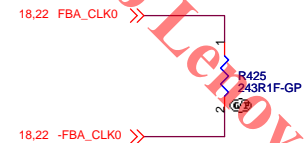
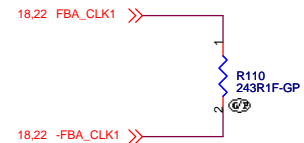
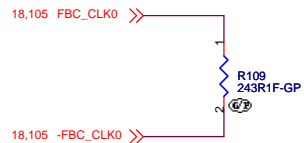
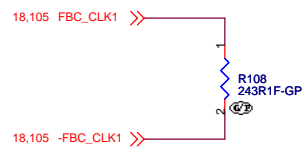
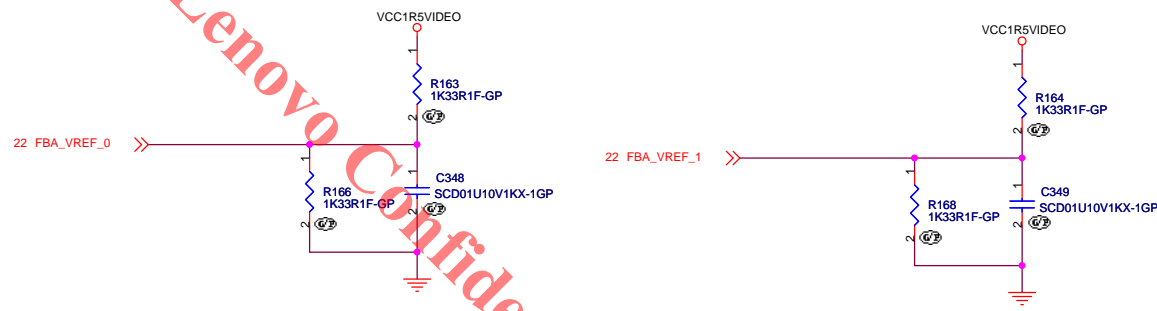


VIDEO FRAME BUFFER PORT A

«Core Design»

緯創資通 Wistron Corporation	
21F, 8th, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
File	VRAM CHANNEL-A
Size	Document Number
AS	Kendo-1 WS
Date	Tuesday, May 05, 2009
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### FBCLK Termination

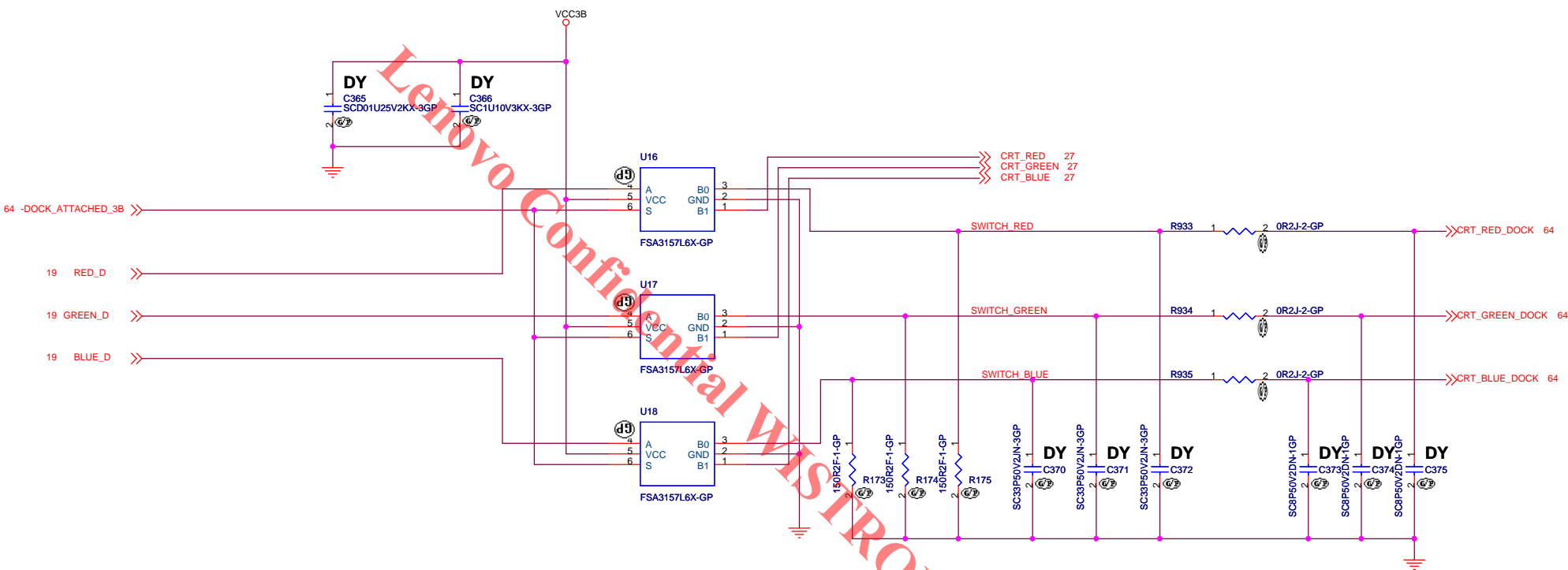
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<b>緯創資通</b> <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title <b>DYNAMIC MEMORY TERMINATION</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009	Sheet 23 of 109	

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BLANK

<Variant Name>	
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title BLANK	
Size A3	Document Number Kendo-1 WS
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	Supplier	Vendo P/N	WISTRON P/N
1	FAIRCHILD	FSA3157L6X-GP	73.03157.G0H
2	ONSEMI	NLASB3157MTR2G	73.03157.003
3	TI	SN74LVC1G3157DRYR	73.03157.H0H

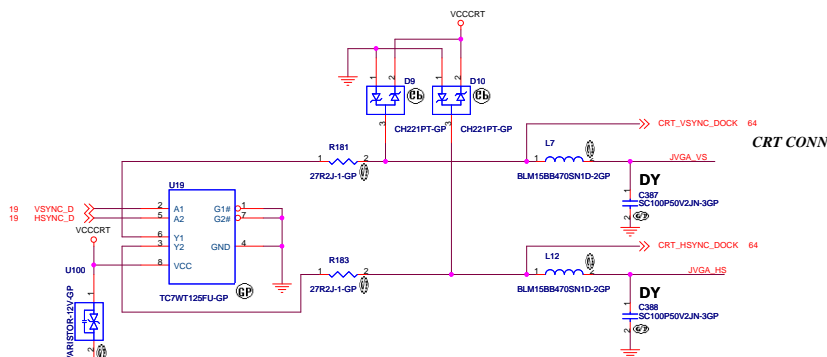
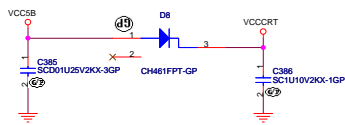
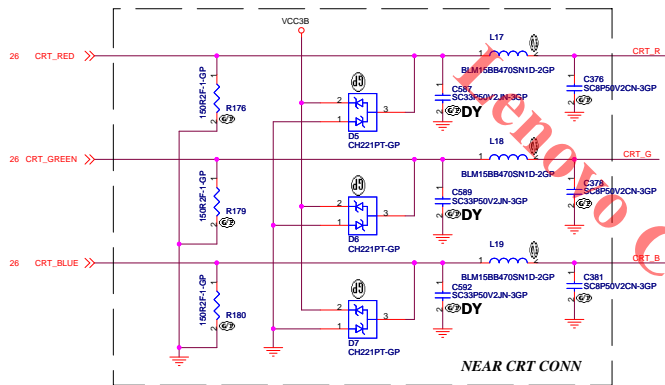
Variant Name: >

**緯創資通** **Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

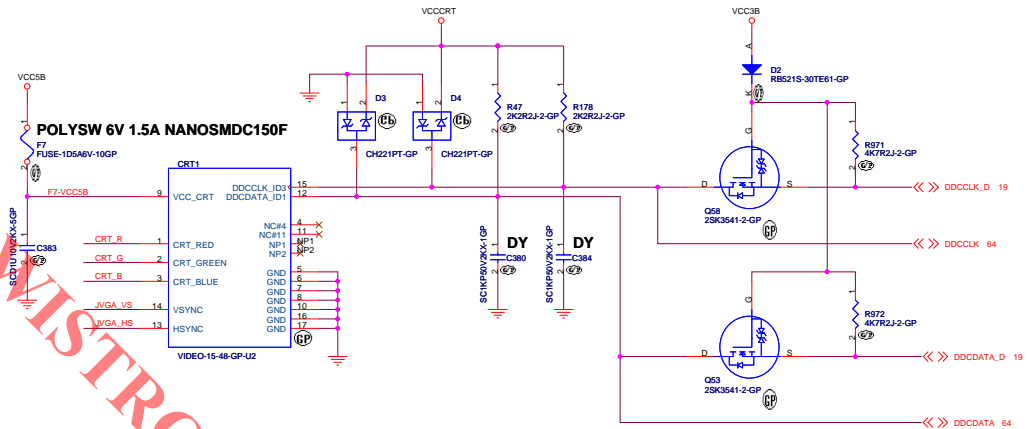
Title: **RGB SWITCH**

Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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	Supplier	Vendo P/N	WISTRON P/N
1	TOSHIBA	TC7WT125FU-GP	73.7W125.007
2	TI	SN74LVC2G125DCTR	73.2G125.007



To avoid leak current from a monitor.

**N10X requires 100K pull-down on AUX signals.**

placed near DP connector

placed near DP connector

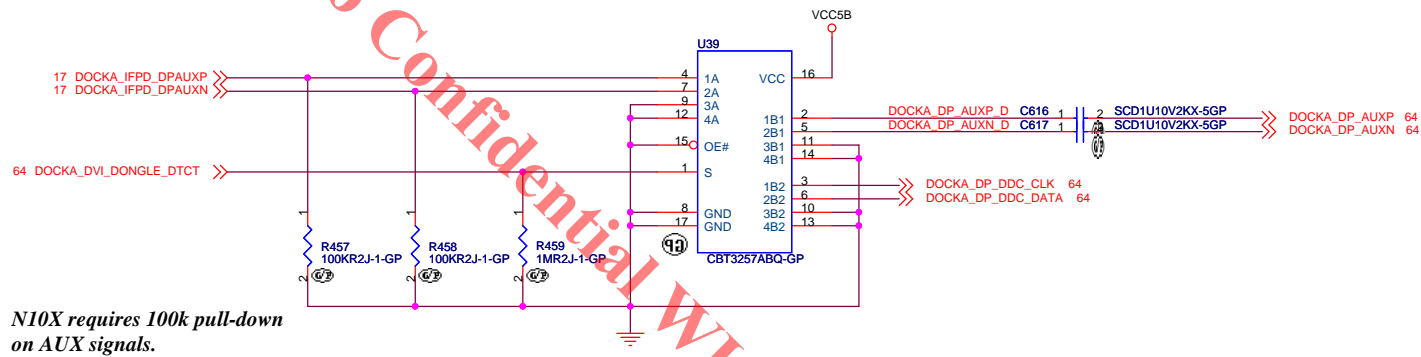
placed near DP connector

placed near DP connector

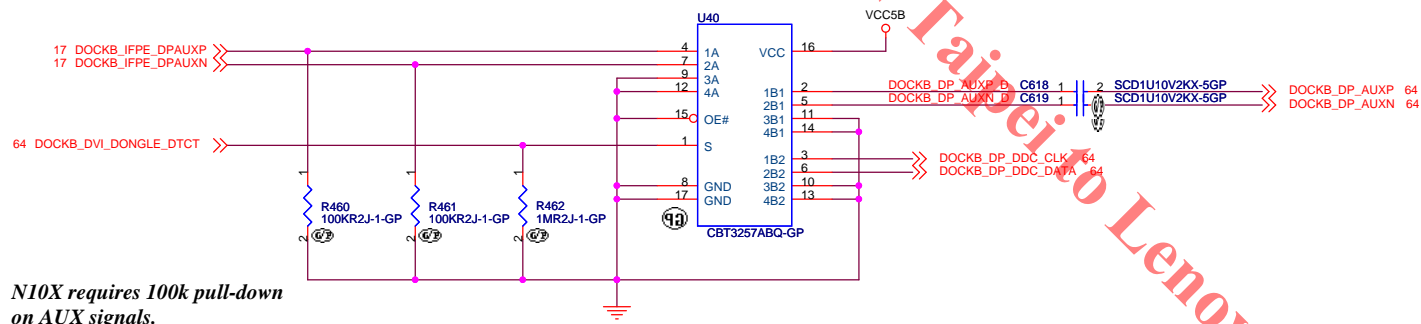
To prevent Leakage current from DP monitor.

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Title		Variant Name	
Size		Document Number	
A3		Kendo-1 WS	
Date		Sheet	
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Title		Rev	
DISPLAY PORT CONNECTOR		SC	
Wistron Corporation		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	



		U27, U39, U40	Wistron part number
1	NXP	CBT3257ABQ-GP	73.03257.A03
2	OnSemi	74FST3257MNTWG-GP	73.5A121.00H



INPUTS		INPUT/OUTPUT		FUNCTION
		OE#	S	
L	L	B1		A port = B1 port
L	H	B2		A port = B2 port
H	X	Z		Disconnect

<Variant Name>

**緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

**DISPLAY PORT MUX**

Size  
A3

Document Number

**Kendo-1 WS**

Rev  
**SC**

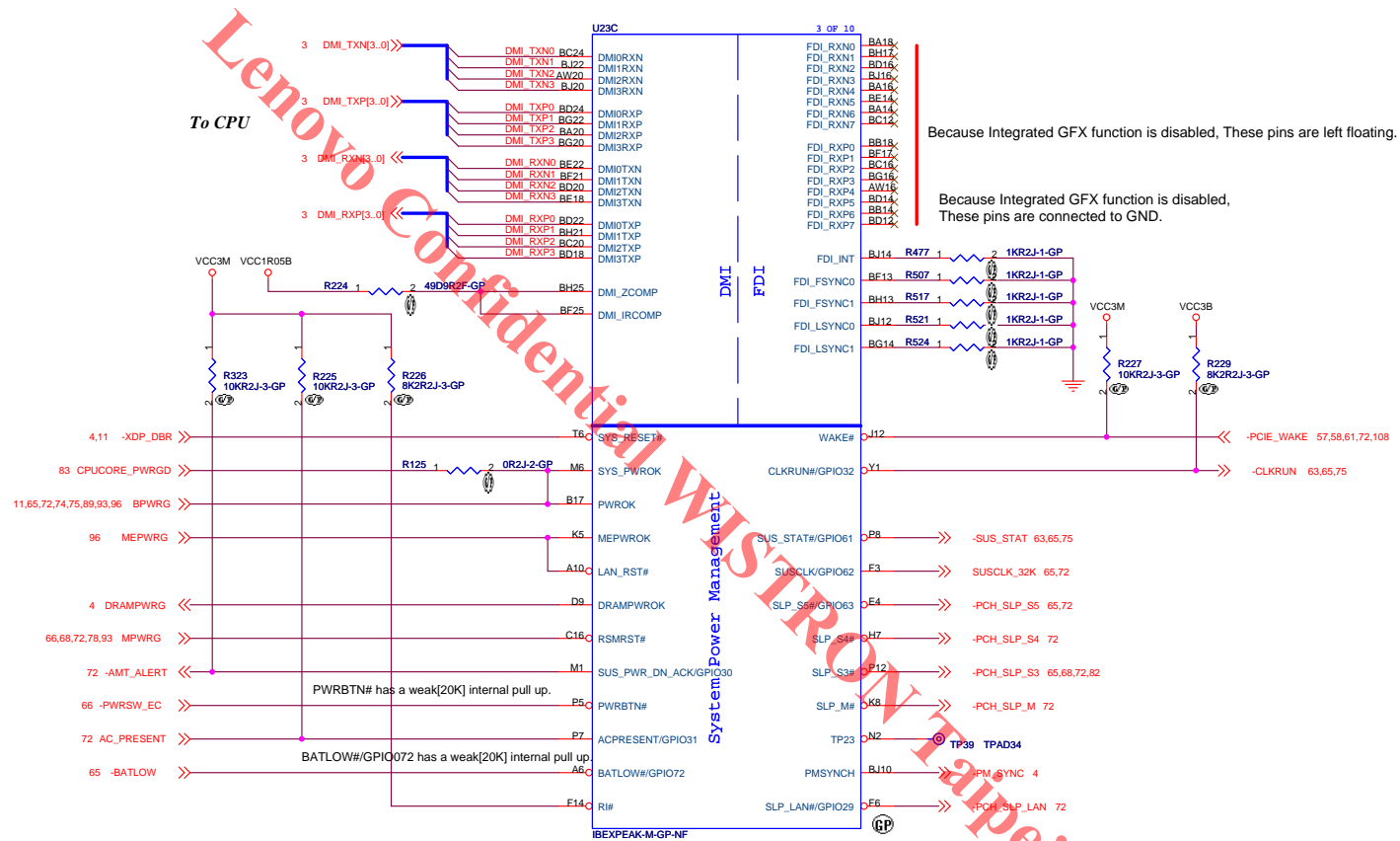
Date: Tuesday, May 05, 2009

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<Variant Name>

緯創資通 Wistron Corporation  
21F, 86, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title **PCH (3/8):DMI/FDI/PM**  
Size Document Number  
Custom **Kendo-1 WS** Rev **SC**  
Date: Tuesday, May 05, 2009 Sheet 32 of 109

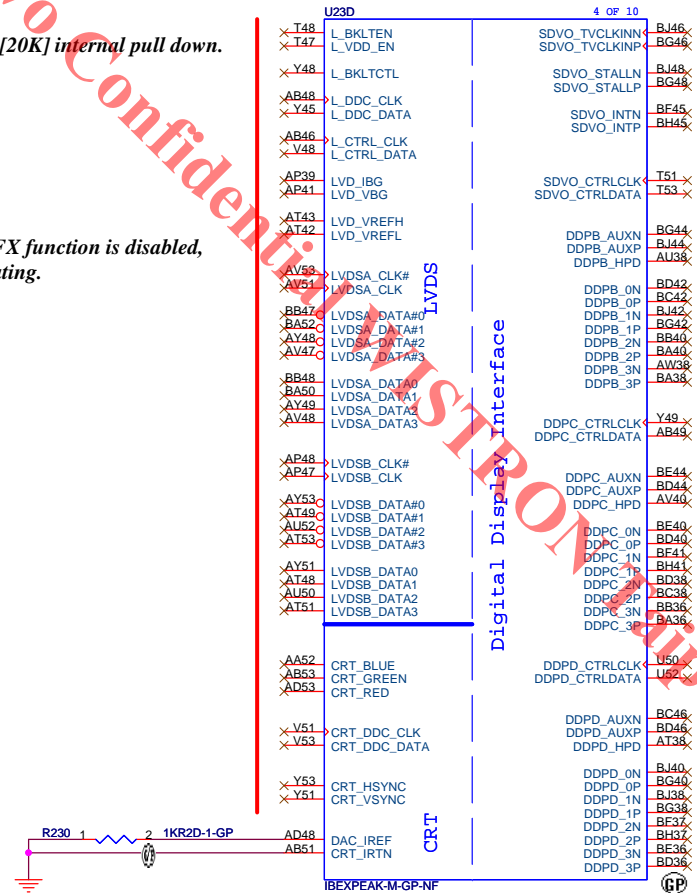
L\_DDC\_DATA has a weak[20K] internal pull down.

Because Integrated GFX function is disabled,  
These pins are left floating.

SDVO\_CTRLCLK/SDVO\_CTRLCLK has a weak[20K] internal pull down.

Because Integrated GFX function is disabled,  
These pins are left floating.

DDP[D:C]\_CTRLCLK has a weak[10K] internal pull down.  
DDP[D:C]\_CTRLDATA has a weak[20K] internal pull down.



緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin,  
Taipei Hsien 221, Taiwan, R.O.C.

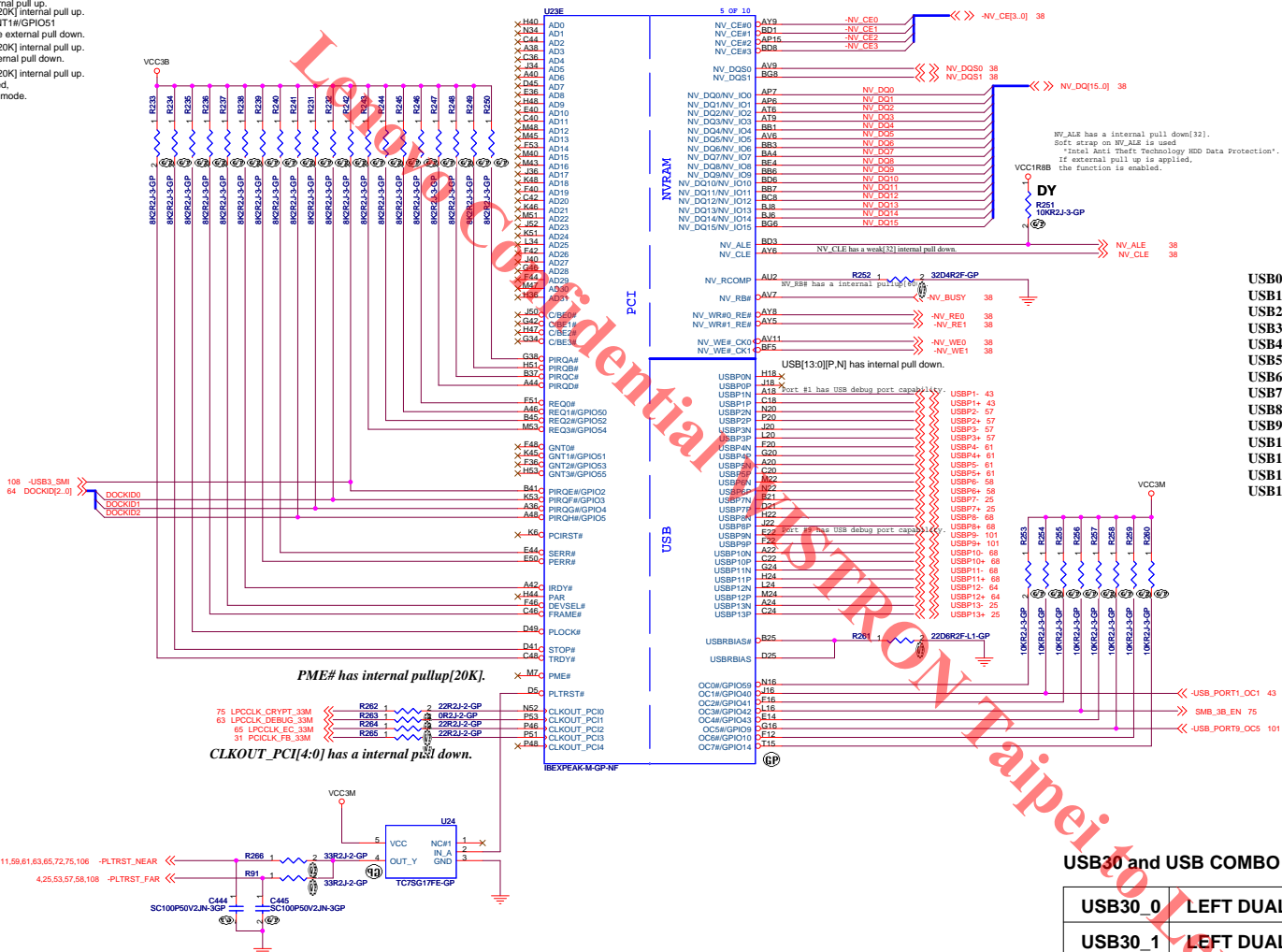
Title PCH (4/8):LVDS/CRT/DDI

Size A3 Document Number Kendo-1 WS Rev SC

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Because PCI IF is not used, ADx.C/BEx and GNTx are left as NC.

GNT0# has a weak[20K] internal pull up.  
GNT1#/GPIO51 has a weak[20K] internal pull up.  
To use SPI IF flash BIOS, GNT1#/GPIO51  
and GNT0# should not place external pull down.  
GNT2#/GPIO53 has a weak[20K] internal pull down.  
This pin should not have external pull down.  
GNT3#/GPIO55 has a weak[20K] internal pull up.  
If external pull down is applied,  
PCH will be "topblock swap" mode.



- USB0 :Reserved
- USB1 : To System onboard USB port(eSATA combo)
- USB2 : To WiMAX/WLAN Mini Card Slot
- USB3 : To WWAN Mini Card Slot
- USB4 : To Smart Card I/F
- USB5 : To Express Card Slot
- USB6 : To WUSB Mini Card Slot
- USB7 : To Touch Screen
- USB8 : To Color Sensor
- USB9 : To System Subcard USB port
- USB10 : To FPR
- USB11 : To Bluetooth
- USB12 : To Docking
- USB13 : To Camera

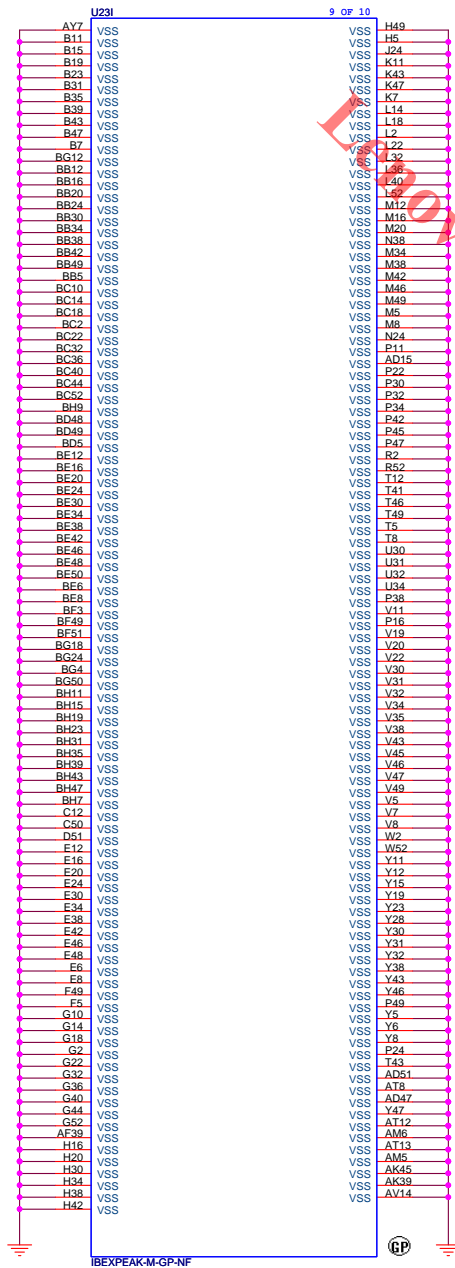
USB30 and USB COMBO Table

USB30_0	LEFT DUAL CONN	USB30_ON0	-USB30_OC0
USB30_1	LEFT DUAL CONN	USB30_ON1	-USB30_OC1
USBP1	LEFT COMBO CONN	USB_ON1	-USB_PORT1_OC1
USBP9	RIGHT SUB CARD	USB_ON2	-USB_PORT9_OC5







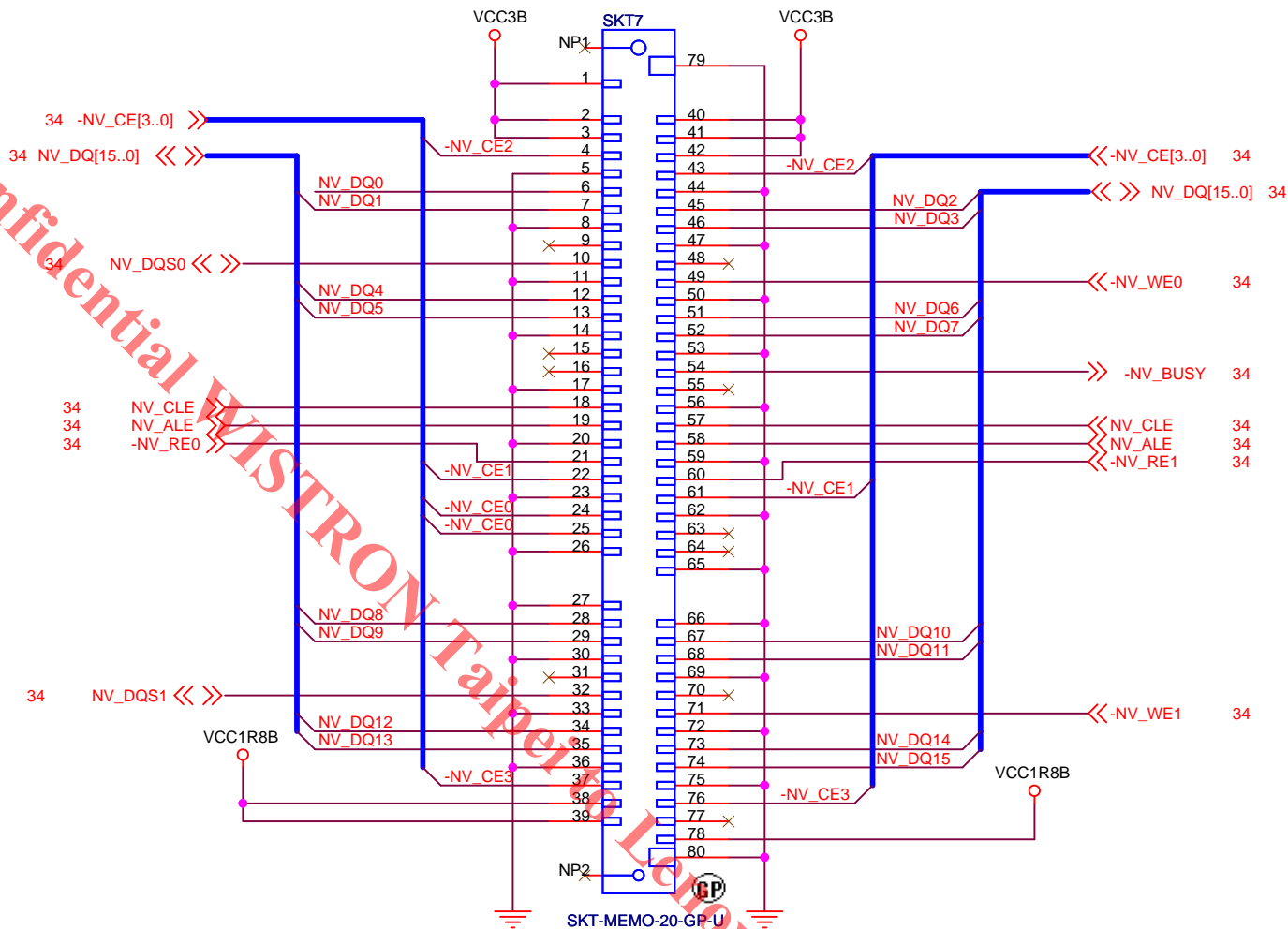
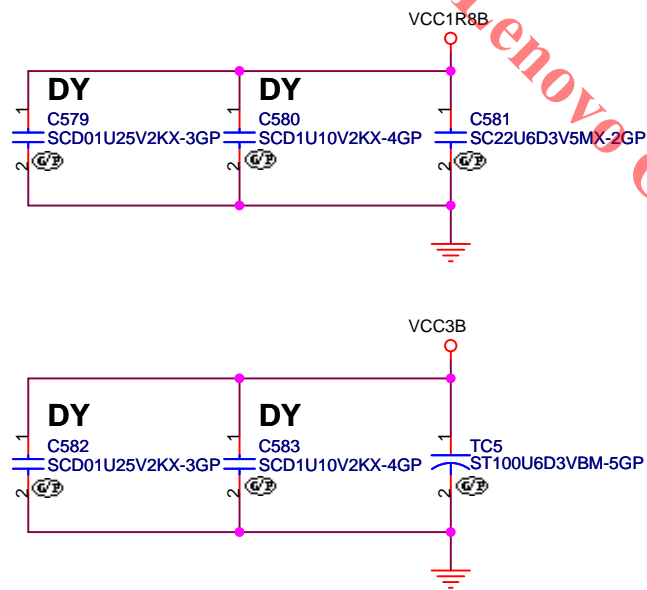


緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title: PCH (8/8):GND

Size: Custom Document Number: Kendo1 WS Rev: SC

Date: 2005-05-05 Sheet: 7 of 109



<Variant Name>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**TURBO MEMORY SLOT**

Size  
A4

Document Number

**Kendo-1 WS**

Rev  
**SC**

Date: Tuesday, May 05, 2009

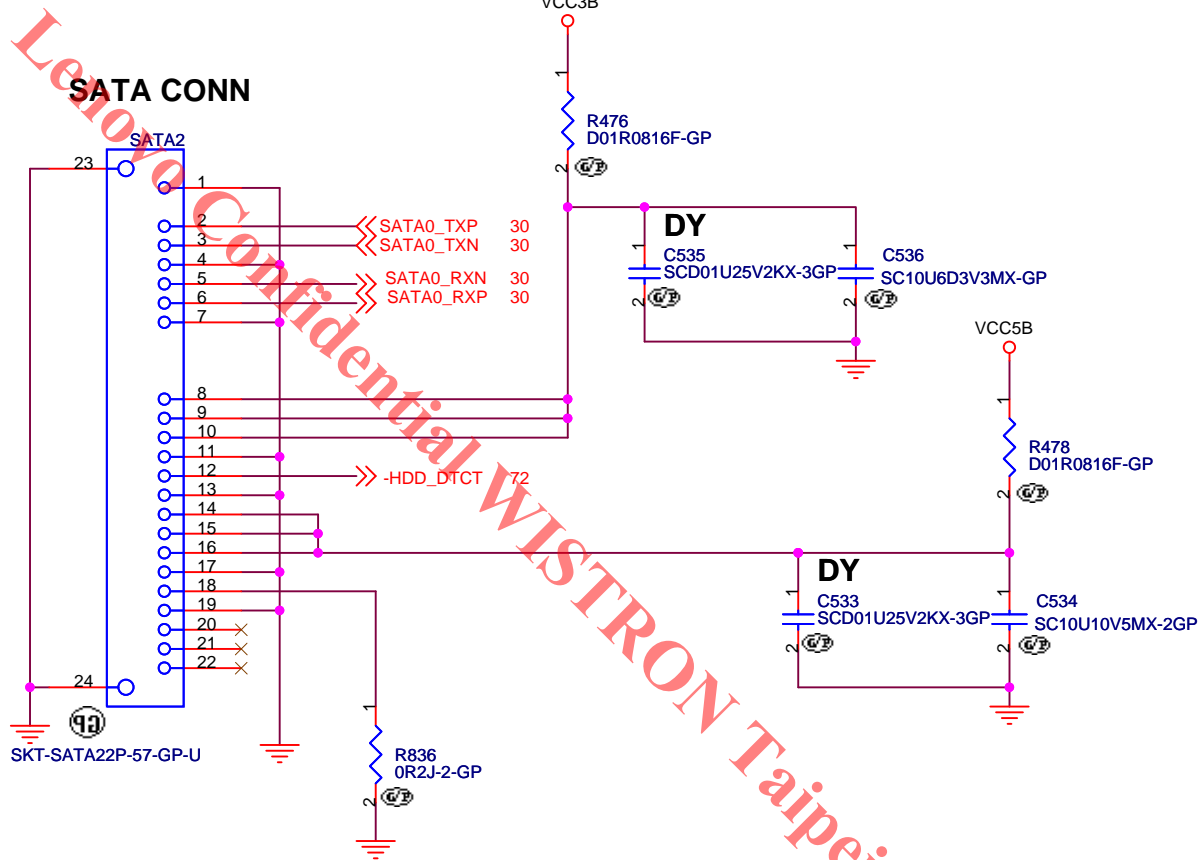
Sheet 38 of 109

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Lenovo Confidential WISTRON Taipei to Lenovo Japan

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Title BLANK		
Size A3	Document Number Kendo-1 WS	Rev SC
Date: Tuesday, May 05, 2009	Sheet 39 of 109	



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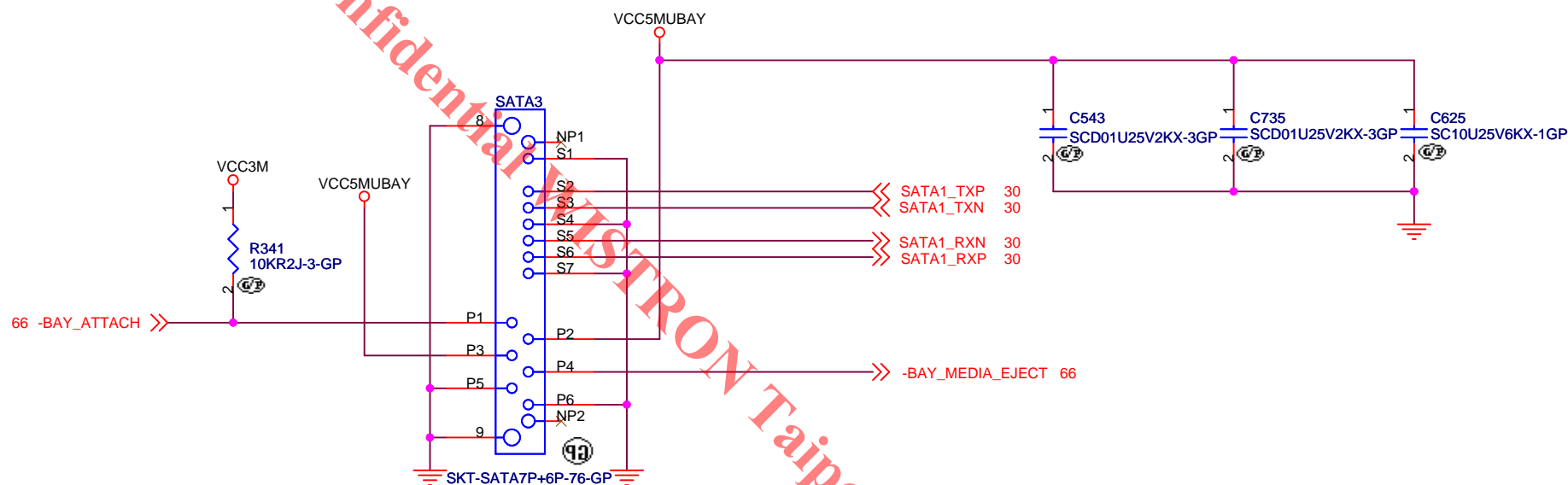
緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title  
**SATA HDD CONN**

Size A4	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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Date: Tuesday, May 05, 2009 Sheet 40 of 109



<Variant Name>

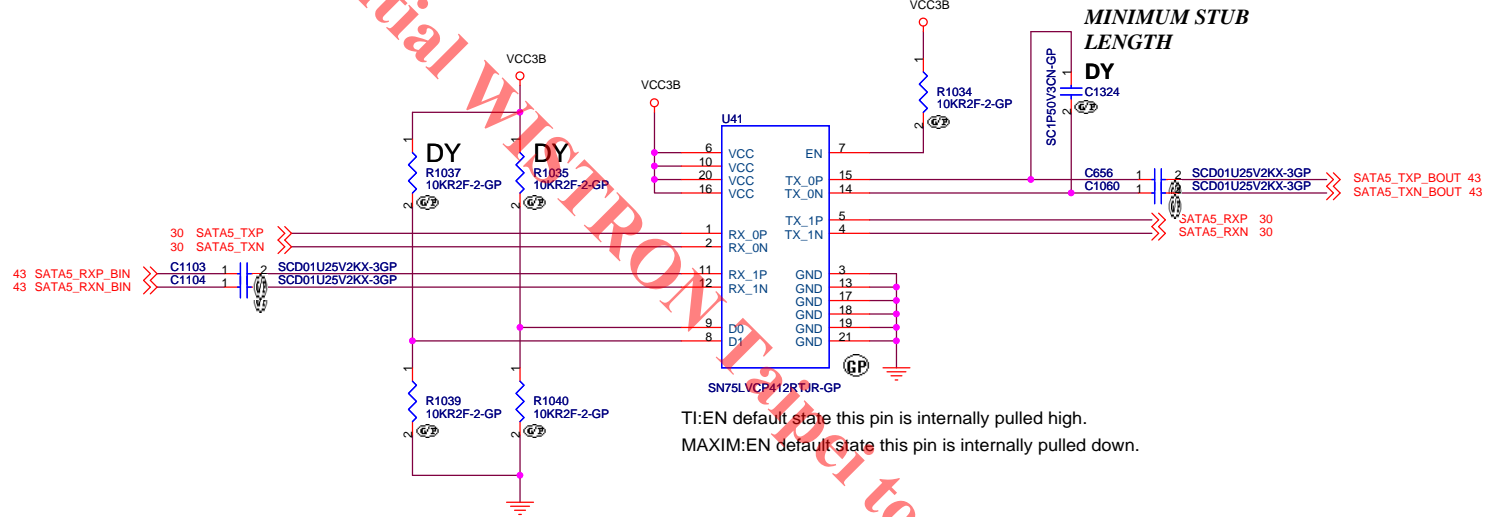
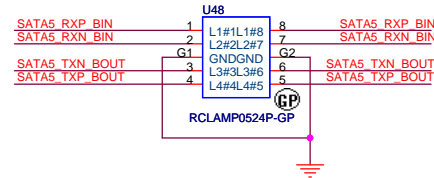
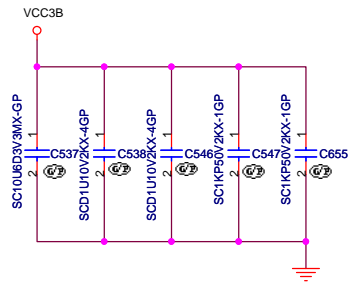
緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title  
**SATA BAY I/F CONN**

Size A4	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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Date: Tuesday, May 05, 2009 Sheet 41 of 109



TABLE

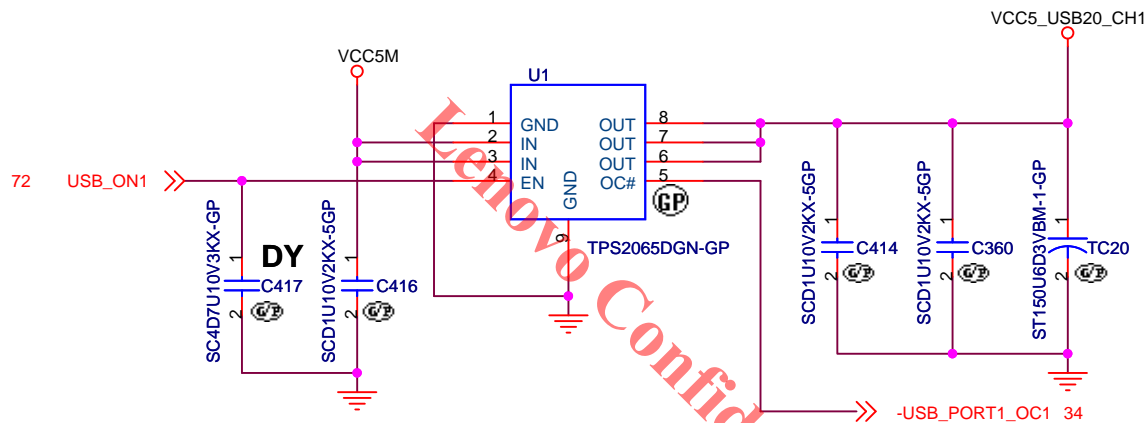
EN	D0	D1	CH - 0	CH - 1
0	X	X	STANDBY	STANDBY
1	0	0	STANDARD	STANDARD
1	1	0	BOOST	STANDARD
1	0	1	STANDARD	BOOST
1	1	1	BOOST	BOOST

← LOGIC

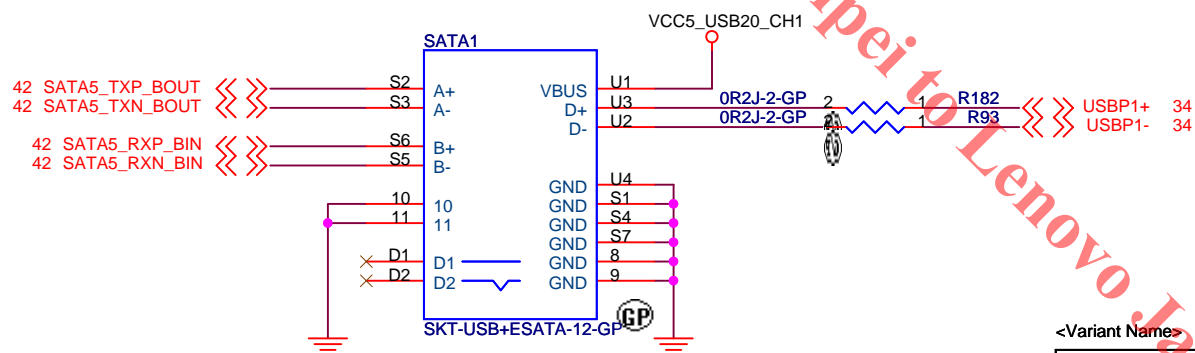
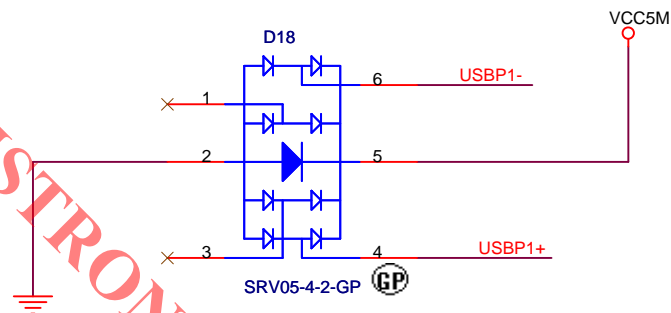
		U41	Wistron part number
1	TI	SN75LVCP412RTJR-GP	71.75412.003
2	MAXIM	MAX4951CTP-T-GP	71.04951.A03

<Core Design>

<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>ESATA CONNECTOR</b>	
Title Size A3	Document Number <b>Kendo-1 WS</b>
Date: Tuesday, May 05, 2009	Sheet 42 of 109
Rev <b>SC</b>	



	Supplier	Vendo P/N	WISTRON P/N
1	TI	TPS2065DGN-GP	74.02065.079
2	TI	TPS2065DGN-1-GP	74.02065.A79
3	ROHM	BD8014FVJ-GP	74.08014.07G



eSATA Combo

<Variant Name>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title **USB POWER/ CONN**

Size A4 Document Number

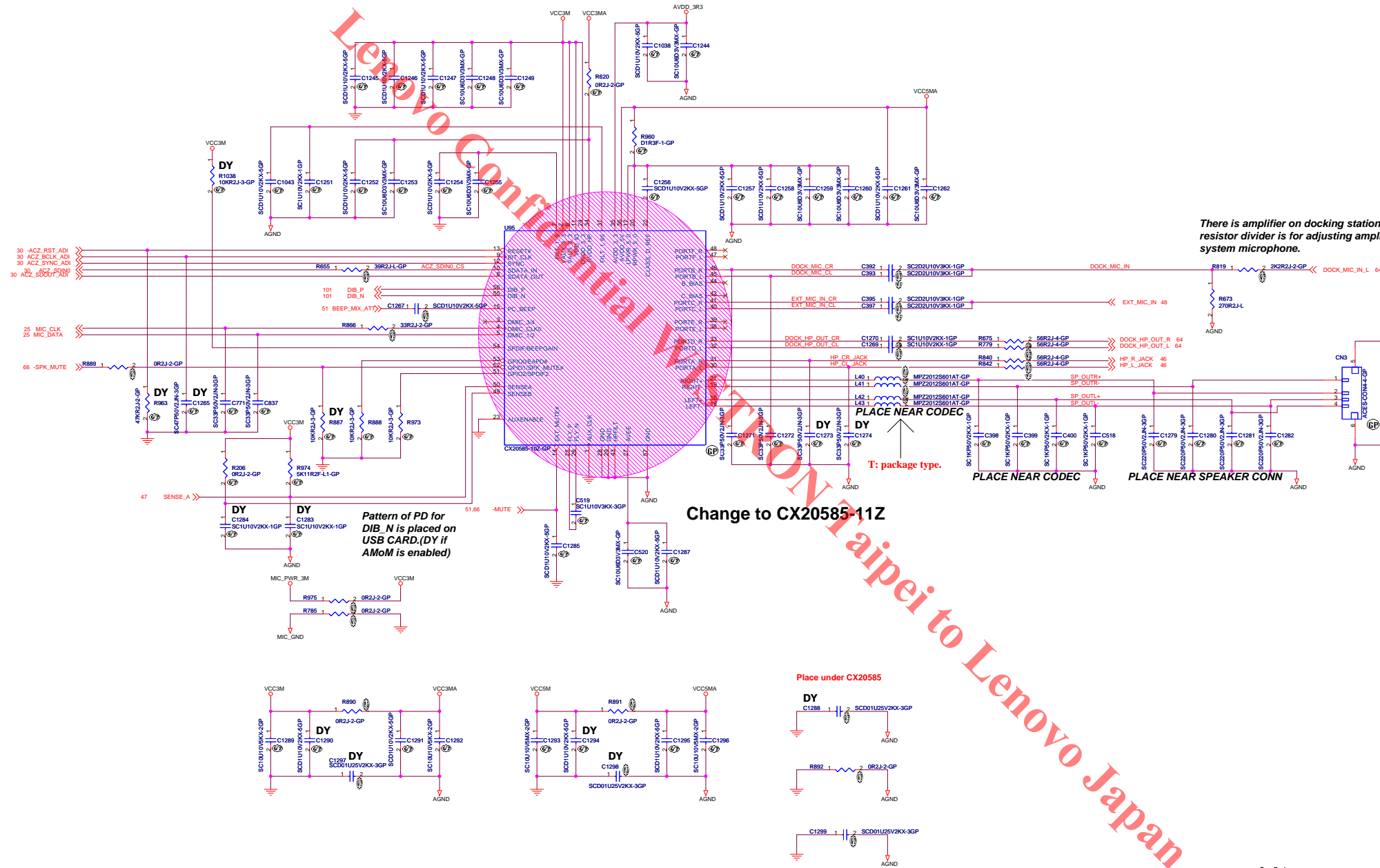
**Kendo-1 WS**

Rev SC

Date: Tuesday, May 05, 2009

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There is amplifier on docking station. This resistor divider is for adjusting amplitude to system microphone.

Pattern of PD for DIB\_N is placed on USB CARD.(DY if AMoM is enabled)

Change to CX20585-11Z

Place under CX20585

PLACE NEAR CODEC  
PLACE NEAR CODEC  
PLACE NEAR SPEAKER CONN

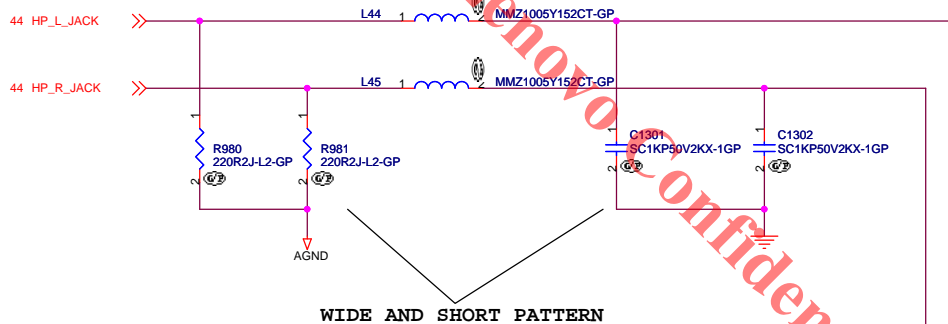


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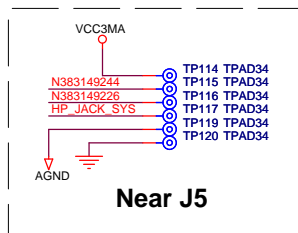
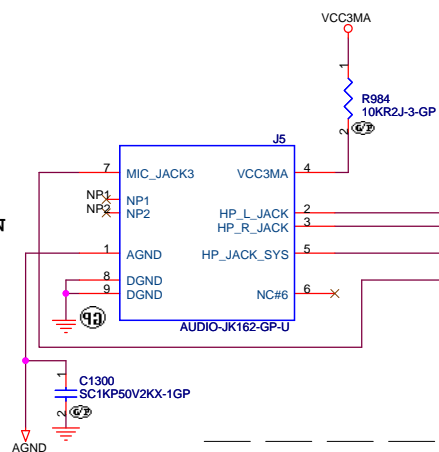
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Size A3	Document Number <b>Kendo-1 WS</b>
Date: Tuesday, May 05, 2009	Sheet 45 of 109

# NEAR HEADPHONE CONN

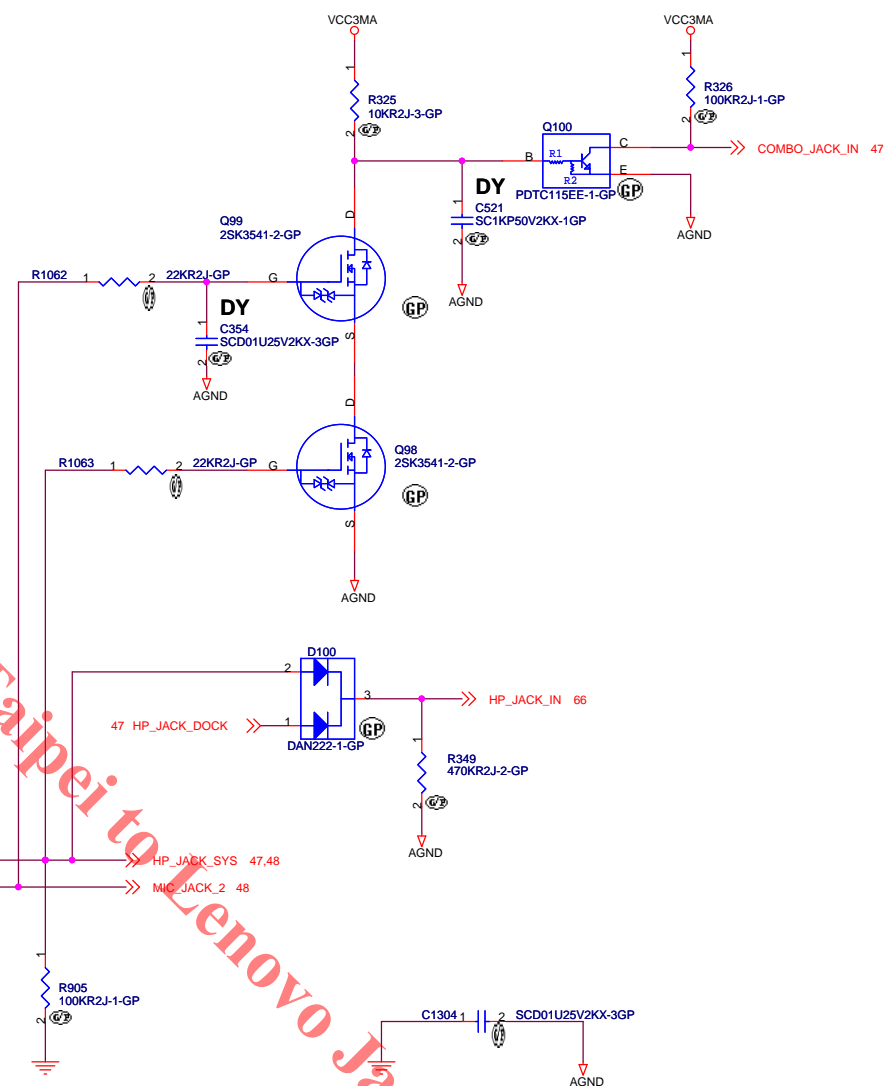


ESD Protection diodes will be change to other parts from SIV phase if it failed.

WIDE PATTERN



	Supplier	Vendo P/N	WISTRON P/N
1	OnSemi	ESD5B5D0ST1G-GP	83.ESD5B.0AF
2	ROHM	RSB5.6S	83.RSB56.0AF



<Core Design>

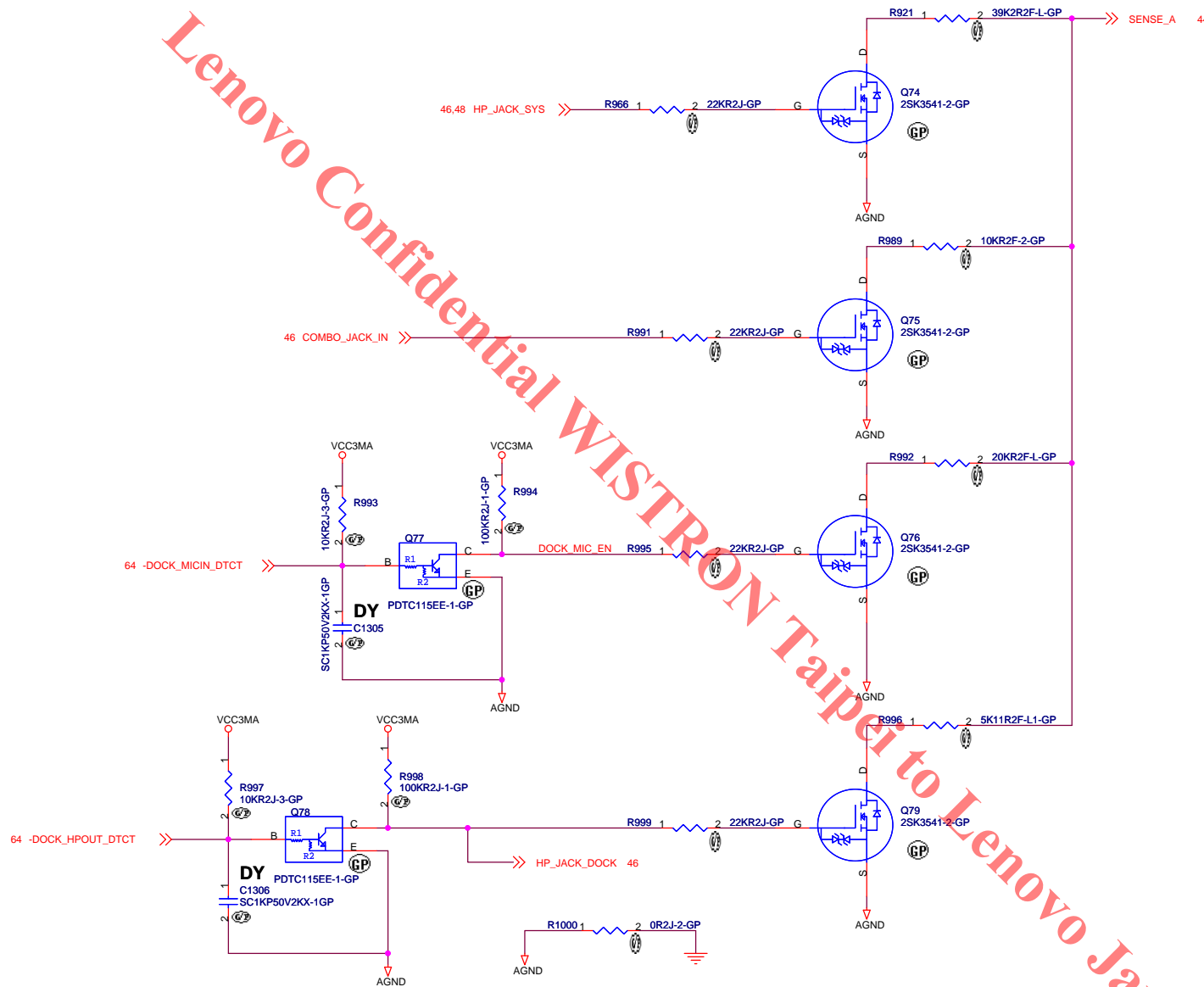
緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title AUDIO CONNECTOR

Size A3 Document Number Kendo-1 WS Rev SC

Date: Tuesday, May 05, 2009 Sheet 46 of 109

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<Core Design>

緯創資通

**Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

Title

**AUDIO JACK SENSE**

Size

Document Number

**Kendo-1 WS**

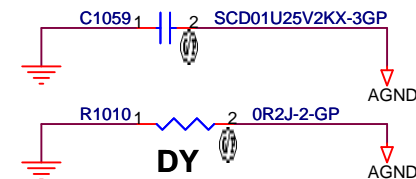
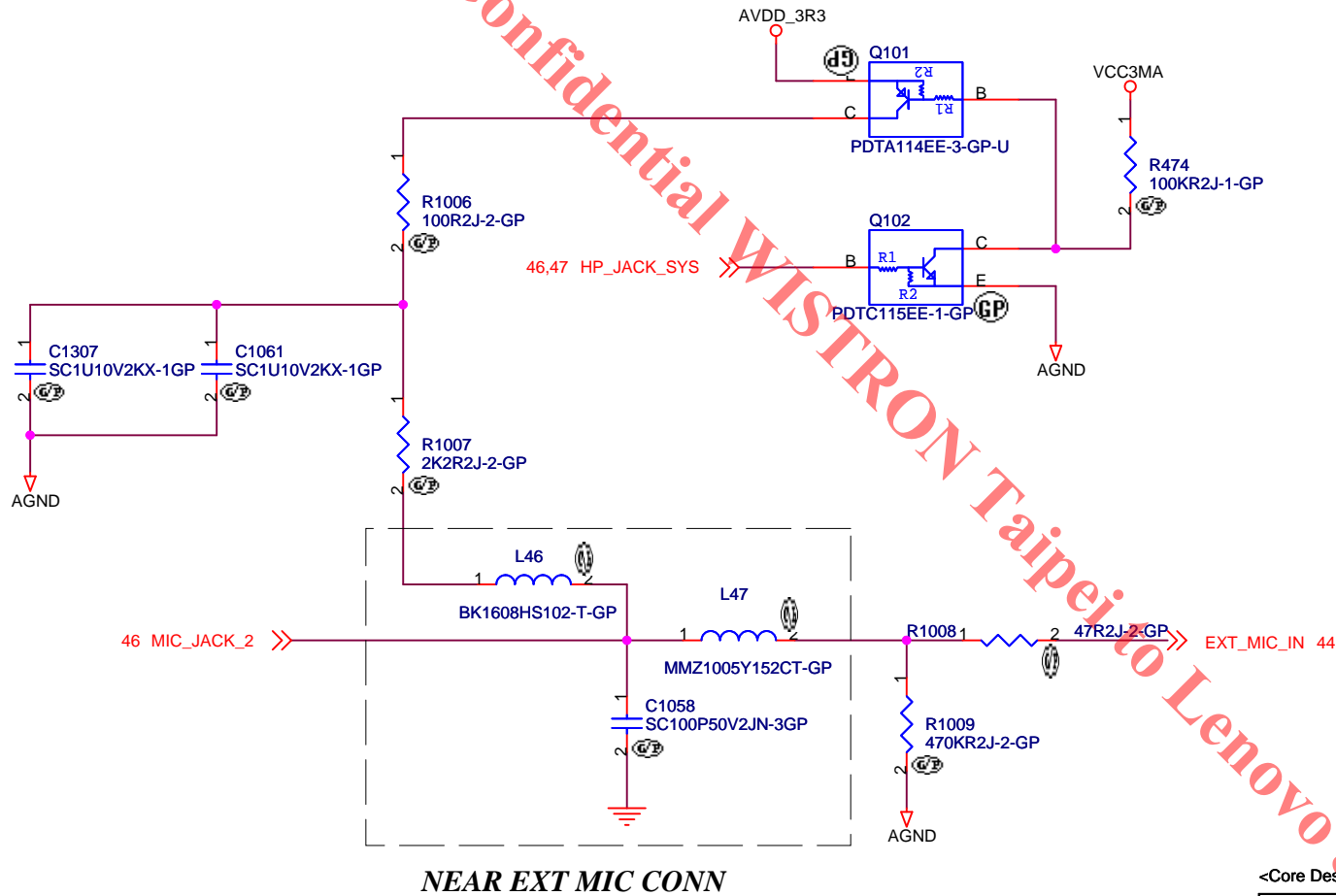
Rev

**SC**

Date: Tuesday, May 05, 2009

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<Core Design>

緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title **AUDIO EXT MIC I/F**

Size  
A4

Document Number

**Kendo-1 WS**

Rev  
**SC**

Date: Tuesday, May 05, 2009

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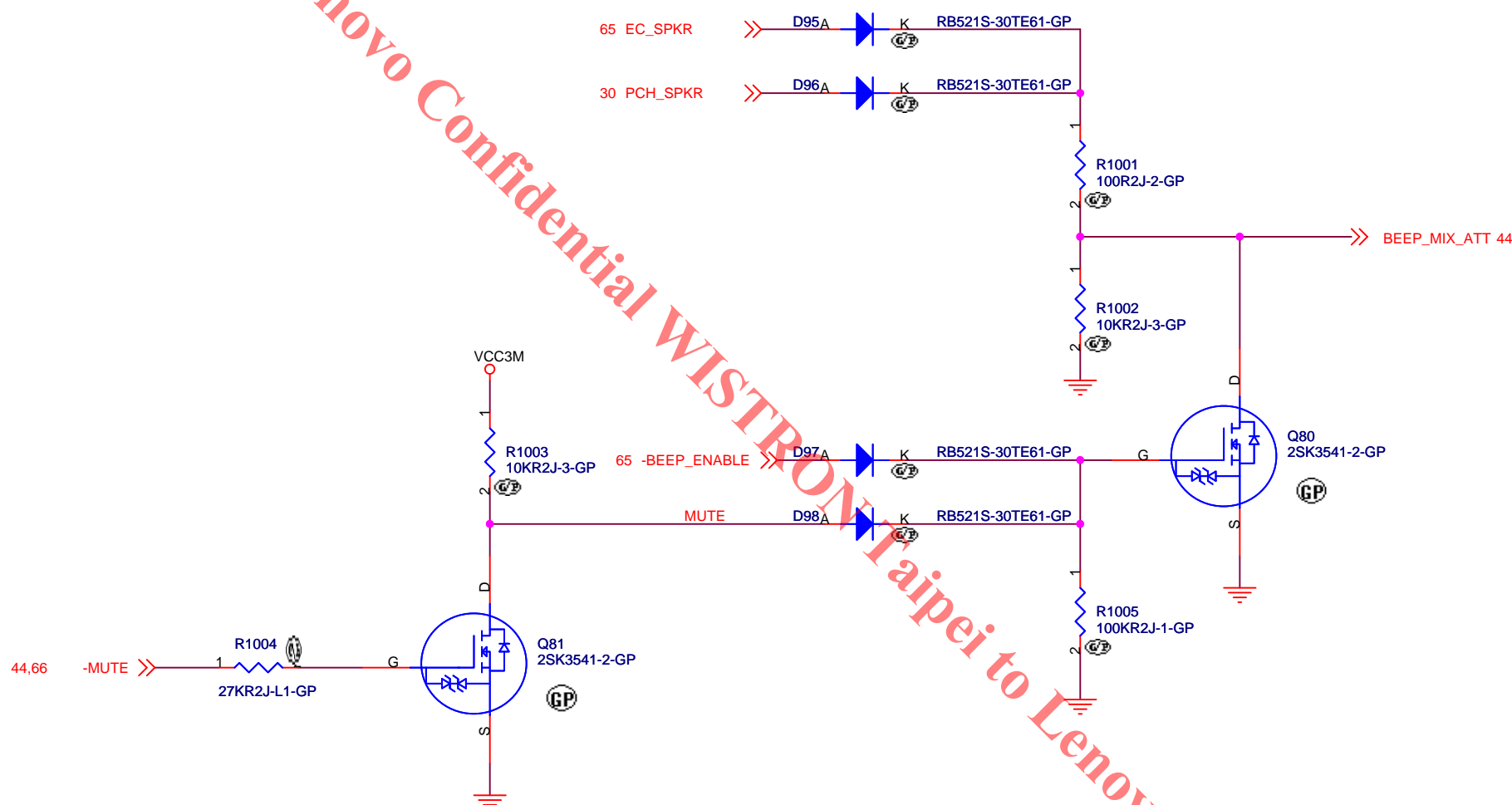
www.vinafix.vn

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Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
Date: Tuesday, May 05, 2009	Sheet 49 of	109

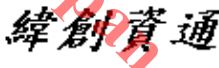
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Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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<Core Design>

		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>BEEP CONTROL</b>			
Size	Document Number		Rev
A4	<b>Kendo-1 WS</b>		<b>SC</b>
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Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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PU is Placed on PCH page.

PU should be VCC3GBE.

SMBUS DEVICE ADDRESSES 0XC8

NOTE: VCC1GBE WILL WORK AT 0.95V TO 1.15V

KDS 25MHz  
18P 30PPM  
DSX321G

	Supplier	Vendo P/N	WISTRON P/N
1	KDS	DSX321G 25M 18P 30PPM	82.30020.B11
2	H.ELE	HSX321S 25M 18P 30PPM	82.30020.B21

KDS Recommended Conditions: Normal Frequency: 25MHz. Frequency Tolerance: +/- 30ppm. Load Frequency: 18pF. Effective Series Resistance: 50-ohm. Effective Shunt Capacitance: 2pF.	HELE Recommended Conditions: Normal Frequency: 25MHz. Frequency Tolerance: +/- 30ppm. Load Frequency: 18pF. Effective Series Resistance: 50-ohm. Effective Shunt Capacitance: 2pF.	Intel Recommended Conditions: Normal Frequency: 25MHz. Frequency Tolerance: +/- 30ppm. Load Frequency: 18pF. Effective Series Resistance: 50-ohm. Effective Shunt Capacitance: 6pF.
--	---	--

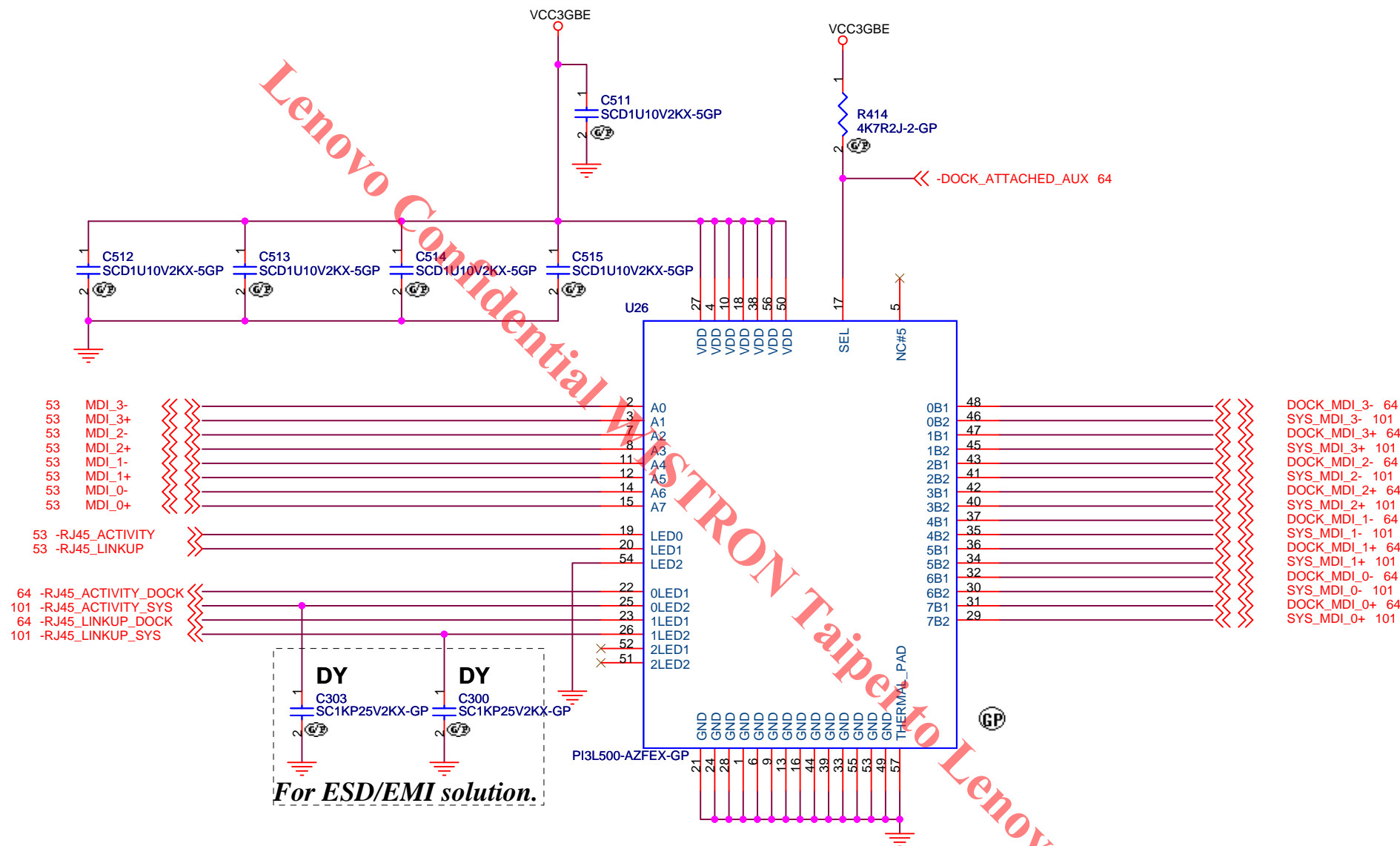
www.vinafix.vn

DEFAULT SOLUTION: SHARDED  
WITH PCH VCC1R05LAN SVR

<Variant Name>

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.


Title <b>GBE HANKSVILLE</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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*For ESD/EMI solution.*

[Source Cadidate]				
1st	Pericom	PI3L500AZFEX	73.3L500.003	41R0539AA
2nd	TI	TS3L500AERHUR	73.3L500.A0V	41R0539BA

<Variant Name>

 <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>Title</b> <b>GBE LAN SW</b>	
Size A4	Document Number <b>Kendo-1 WS</b>
Date: Tuesday, May 05, 2009	
Rev <b>SC</b>	
Sheet 54 of 109	

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Lenovo Confidential WISTRON Taipei to Lenovo Japan

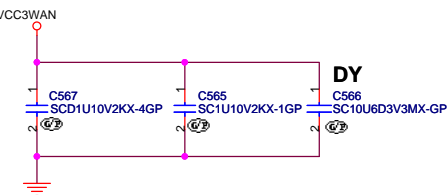
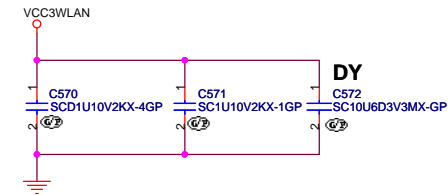
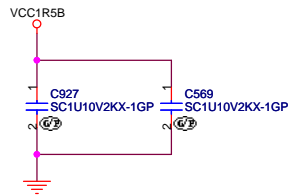
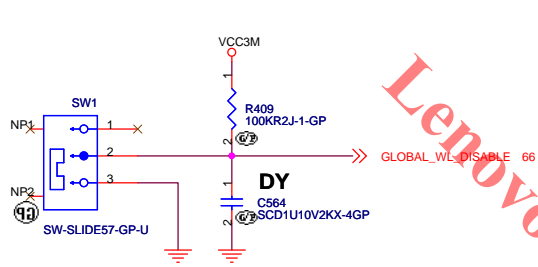
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Lenovo Confidential WISTRON Taipei to Lenovo Japan

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Title BLANK		
Size A3	Document Number Kendo-1 WS	Rev SC
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# WIRELESS DISABLE SWITCH

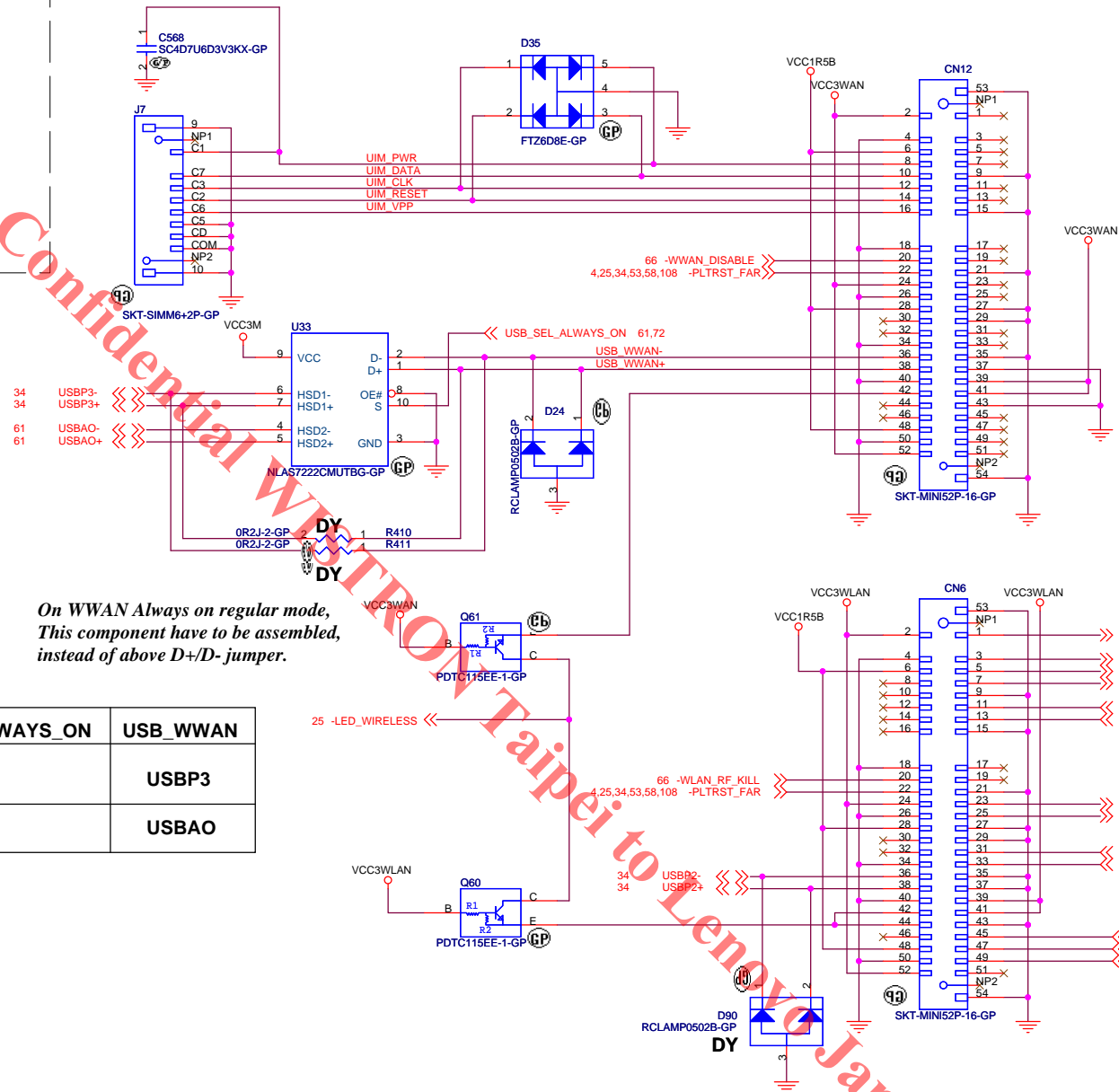


Always ON	YES	NO
U33	ASM	No_ASM
R410	No_ASM	ASM
R411	No_ASM	ASM

↑  
LOGIC

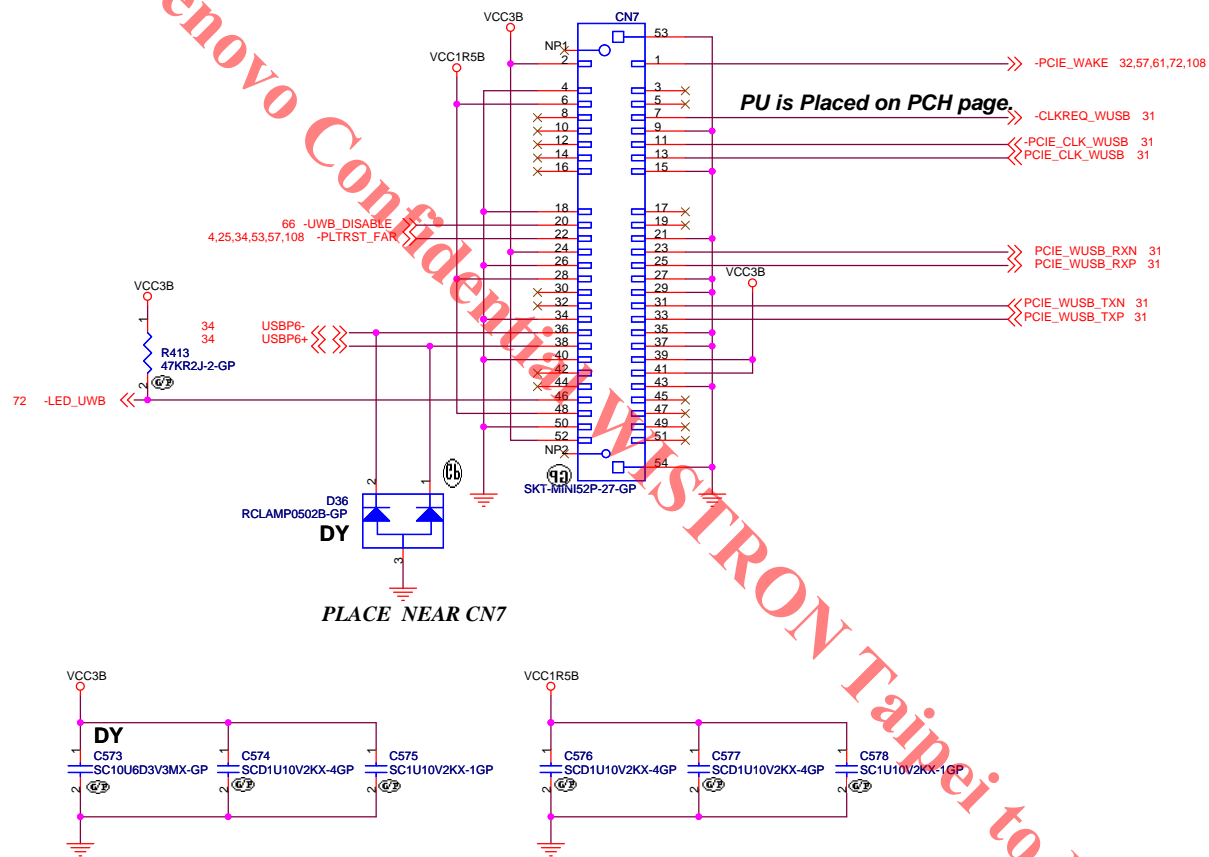
On WWAN Always on regular mode,  
This component have to be assembled,  
instead of above D+/D- jumper.

USB_SEL_ALWAYS_ON	USB_WWAN
L	USBP3
H	USBAO

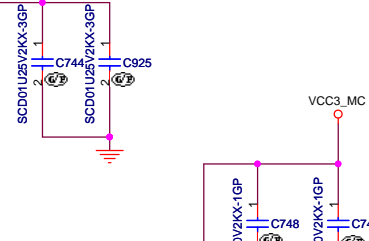
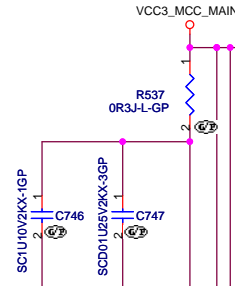
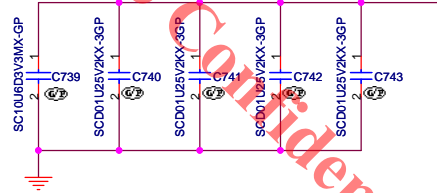
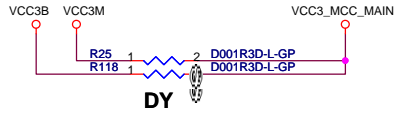


# HALF MINI CARD FOR WUSB

## 7.0H CONNECTOR



	Vendor P/N	Wistron P/N
LED1	LTST-S270KGKT	83.00270.B70
	SML-A10MTT86	83.00A10.070



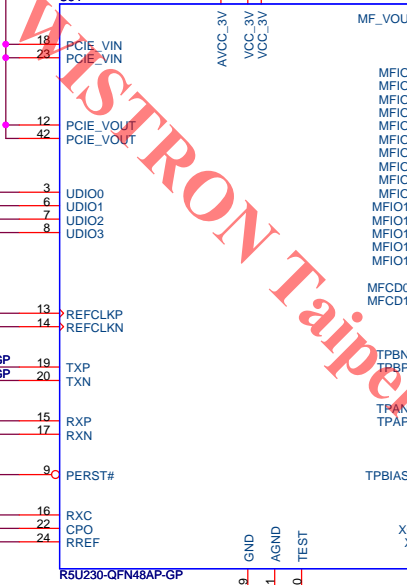
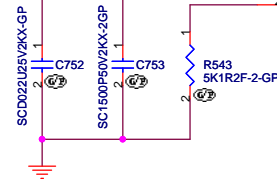
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31 PCIE\_CLK\_MCC <<  
31 -PCIE\_CLK\_MCC <<

31 PCIE\_MCC\_RXP <<  
31 PCIE\_MCC\_RXN <<  
31 PCIE\_MCC\_TXP <<  
31 PCIE\_MCC\_TXN <<

11,34,61,63,65,72,75,106 -PLTRST\_NEAR <<



MFIO0 25  
MFIO1 26  
MFIO2 27  
MFIO3 28  
MFIO4 29  
MFIO5 30  
MFIO6 31  
MFIO7 32  
MFIO8 33  
MFIO9 34  
MFIO10 35  
MFIO11 36  
MFIO12 37  
MFIO13 38  
MFIO14 39  
MFCD0# 40  
MFCD1# 41

TPBNO 45  
TPBP0 46  
TPANO 47  
TPAP0 48  
TPBIAS0 49

XO 43  
XI 44

MFIO0 60  
MFIO01 60  
MFIO02 60  
MFIO05 60  
MFIO08 60  
MFIO10 60  
MFIO11 60  
MFIO14 60  
-MFCD0 60  
-MFCD1 60

TPBNO 45  
TPBP0 46  
TPANO 47  
TPAP0 48  
TPBIAS0 49

XO 43  
XI 44

KDS 24.576MHz  
12P 50PPM  
HCX-6F

MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MS_D0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_R/B
MFCD0#	SDCD#		XDCD0#
MFCD1#		MSINS#	XDCD1#

Richo Recommended Conditions:  
Normal Frequency: 24.576MHz  
Frequency Tolerance: +/- 50ppm.  
Load Capacitance: 10pF.  
Effective Series Resistance: 50-ohm.  
Effective Shunt Capacitance: 7pF.

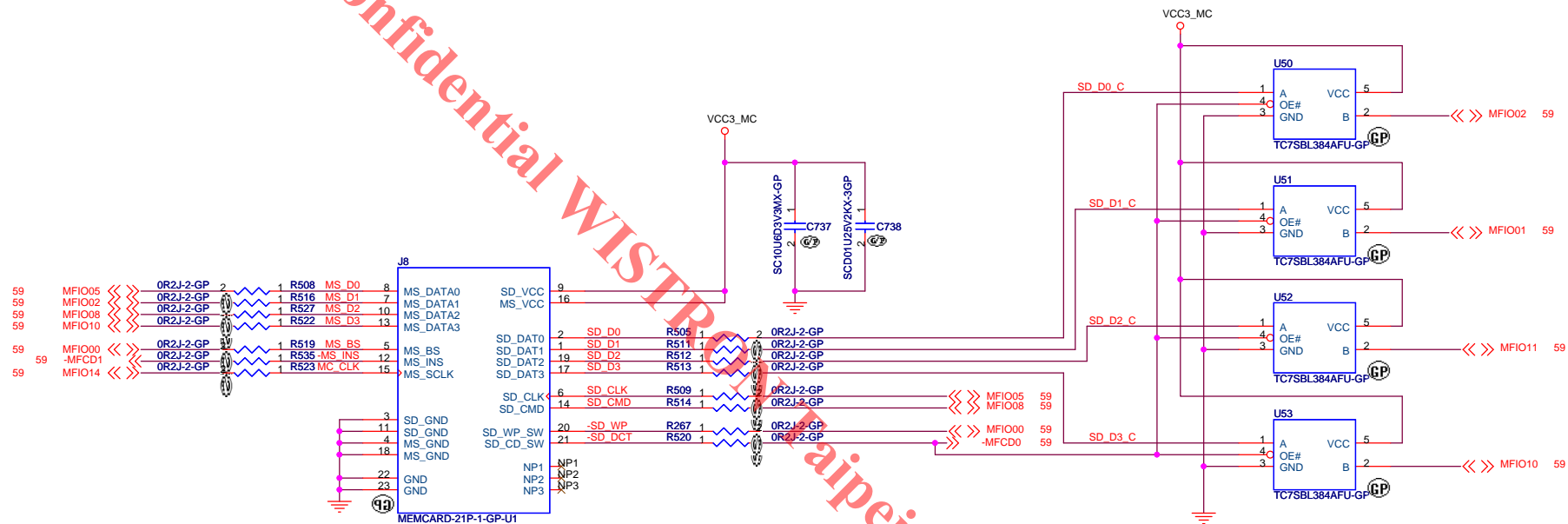
KDS Recommended Conditions:  
Normal Frequency: 24.576MHz  
Frequency Tolerance: +/- 30ppm.  
Load Capacitance: 12pF+/-0.2.  
Effective Series Resistance: 50-ohm.  
Effective Shunt Capacitance: 7pF.

Variant Name: 緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title: 1394/MEDIA CARD CONTROLLER

Size A3 Document Number Kendo-1 WS Rev SC

Date: Tuesday, May 05, 2009 Sheet 59 of 109



		U50, U51, U52, U53	Wistron part number
1	TOSHIBA	TC7SBL384AFU-GP	73.7S384.007
2	NXP	74LVC1G384GW	73.1G384.AHH
3	TI	TS5A3167DCKR	74.53167.A9F

<Variant Name>

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			
MEDIA CARD INTERFACE			
Size	Document Number	Rev	
A3	Kendo-1 WS	SC	
Date:	Tuesday, May 05, 2009	Sheet	60 of 109



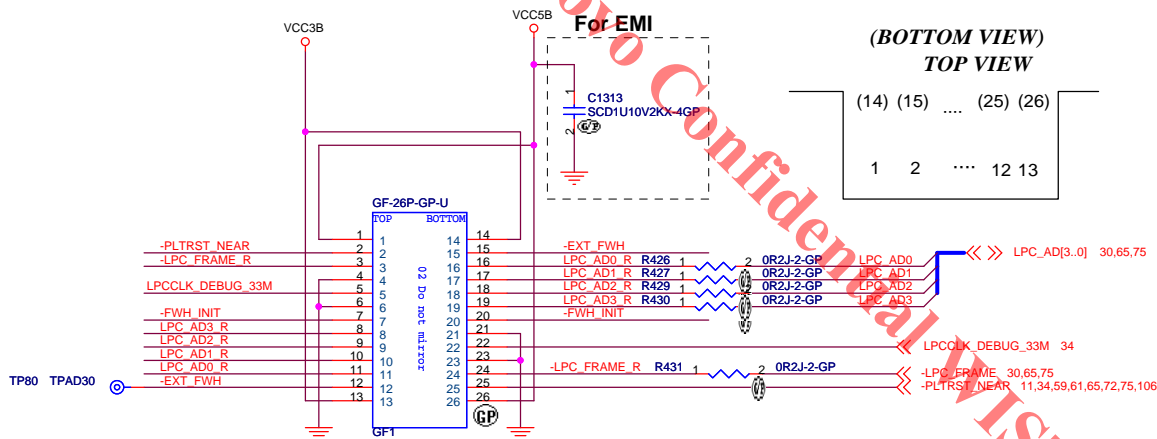


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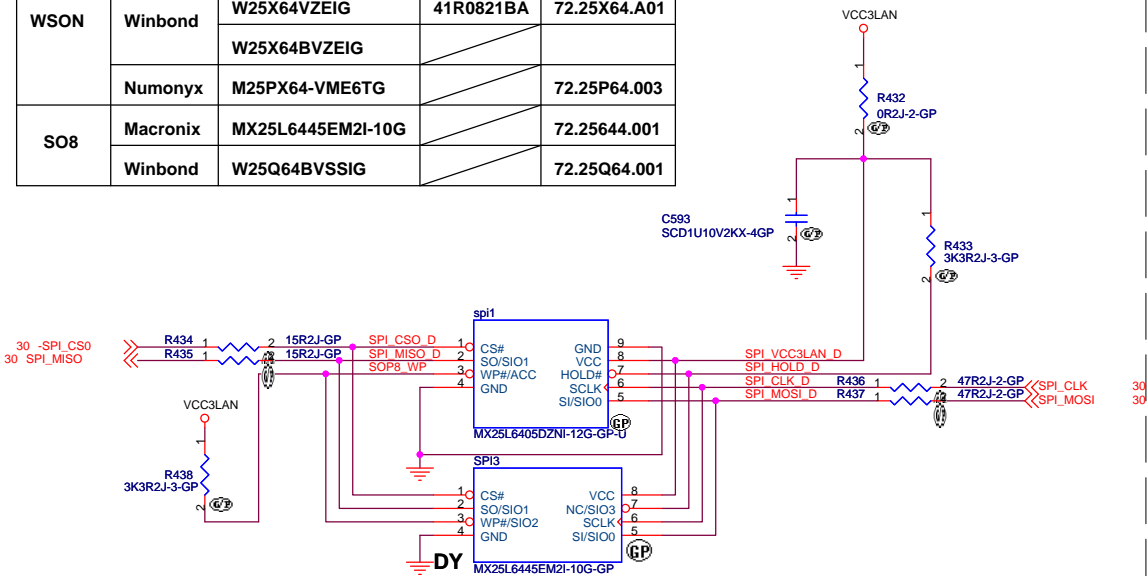
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Size A3	Document Number <b>Kendo-1 WS</b>
Date: Tuesday, May 05, 2009	Sheet 62 of 109
Rev <b>SC</b>	

## Golden Finger for Debug Board



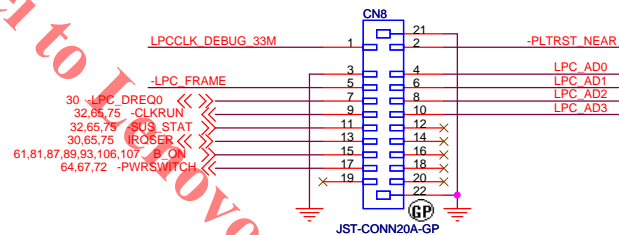
**64Mbit SPI FLASH :**

Package	Supplier	Vendor P/N	Lenovo P/N	Wistron P/N
WS0N	Macronix	MX25L6405DZNI-12G	41R0821AA	72.25640.B01
	Winbond	W25X64VZEIG	41R0821BA	72.25X64.A01
		W25X64BVZEIG		
	Numonyx	M25PX64-VME6TG		72.25P64.003
SO8	Macronix	MX25L6445EM2I-10G		72.25644.001
	Winbond	W25Q64BVSSIG		72.25Q64.001



**Dual foot print for WSON and SO8.**

***Lenovo Debug Tool IF.***



<Variant Name>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title	<b>SPI FLASH</b>
-------	------------------

Size	Document Number
------	-----------------

## Kendo-1 WS

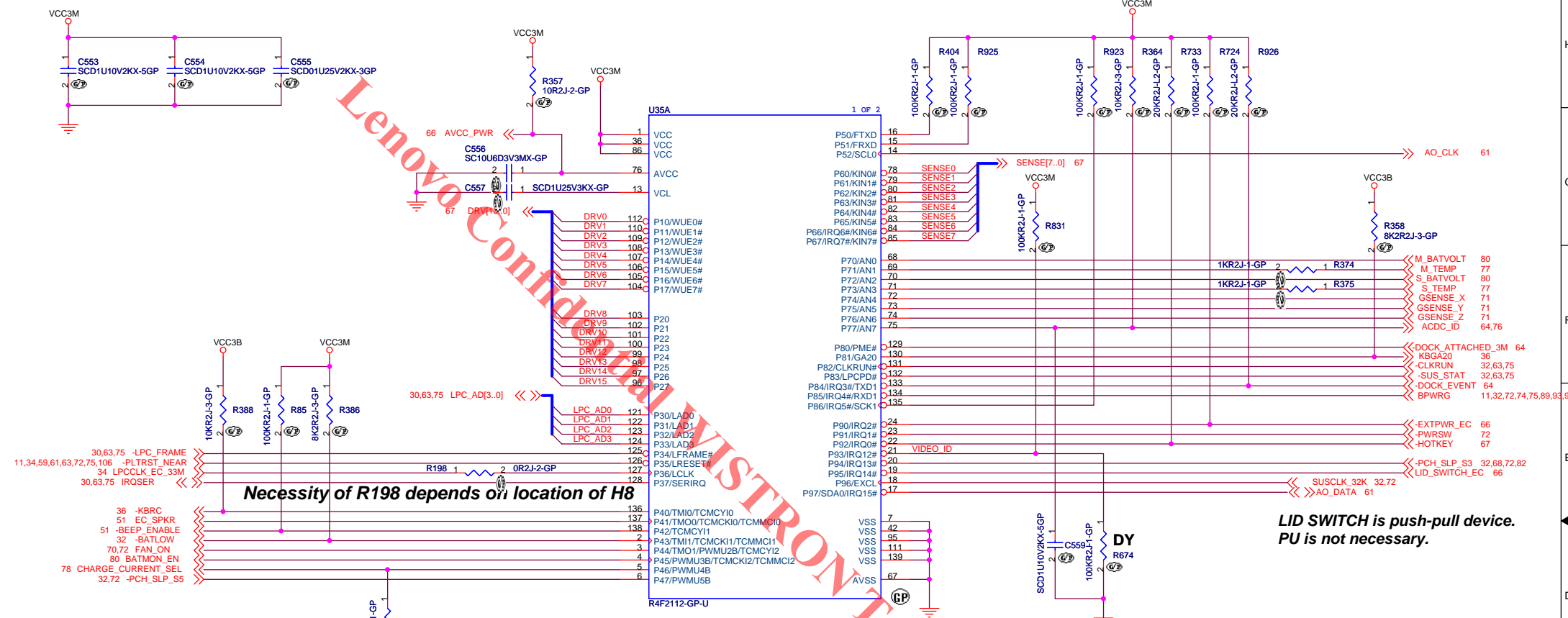
Rev	SC
-----	----

Date: Tuesday, May 05, 2009

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of 109





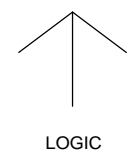
30,63,75 -LPC\_FRAME  
11,34,59,61,63,72,75,106 -PLTRST\_NEAR  
34 LPCCLK\_EC\_33M  
30,63,75 IRQSER

36 -KBRC  
51 EC\_SPKR  
51 -BEEP\_ENABLE  
32 -BATLOW  
70,72 FAN\_ON  
80 BATMON\_EN  
78 CHARGE\_CURRENT\_SEL  
32,72 -PCH\_SLP\_S5

Necessity of R198 depends on location of H8

Table

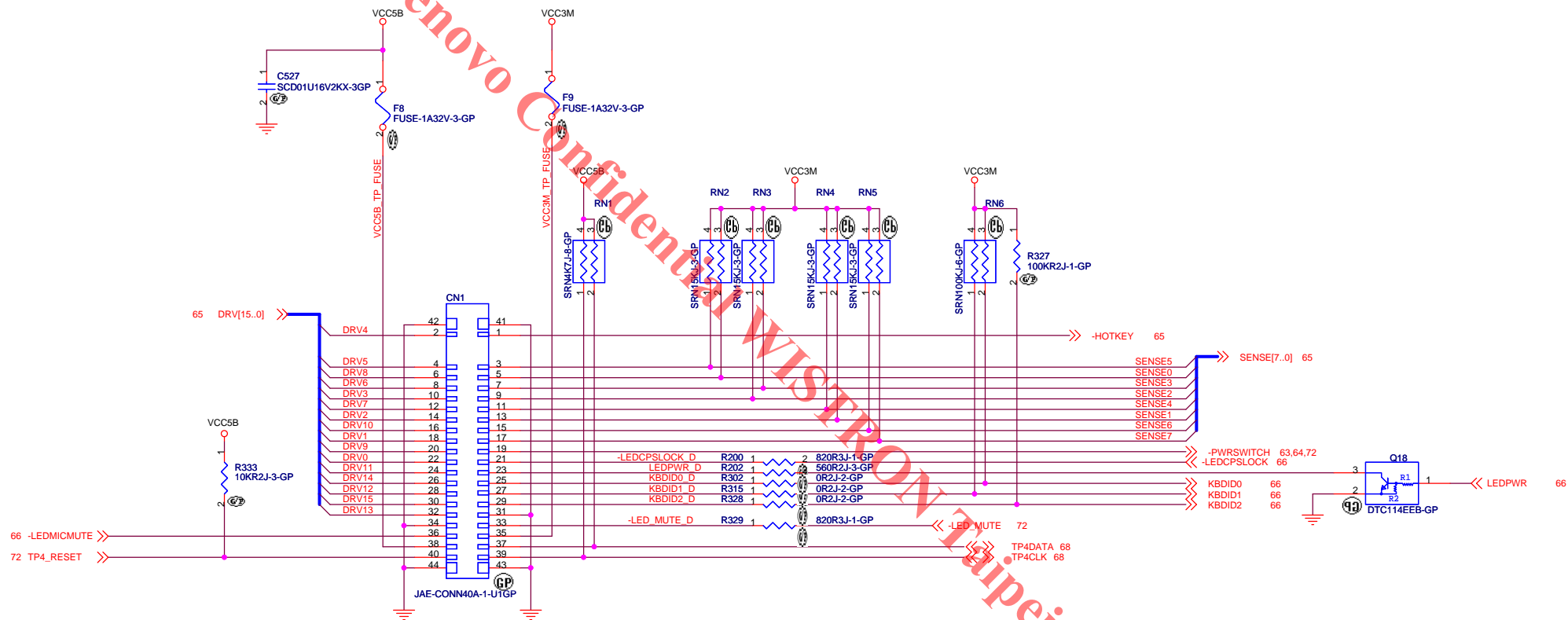
GFX	EXT.	INT.
R831	ASM	NO_ASM
R674	NO_ASM	ASM



LID SWITCH is push-pull device.  
PU is not necessary.



# Keyboard Connector



HOTKEY# IPDCLK  
 DRV4 to R425/Q18

Keyboard Connector Top View

Near CN1

-PWRSWITCH TP81 TPAD60

Variant Name:

Title		Wistron Corporation	
Size		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Document Number		KEYBOARD CONN	
Date		Kendo-1 WS	
Sheet		Rev	
Tuesday, May 05, 2009		SC	
67		109	

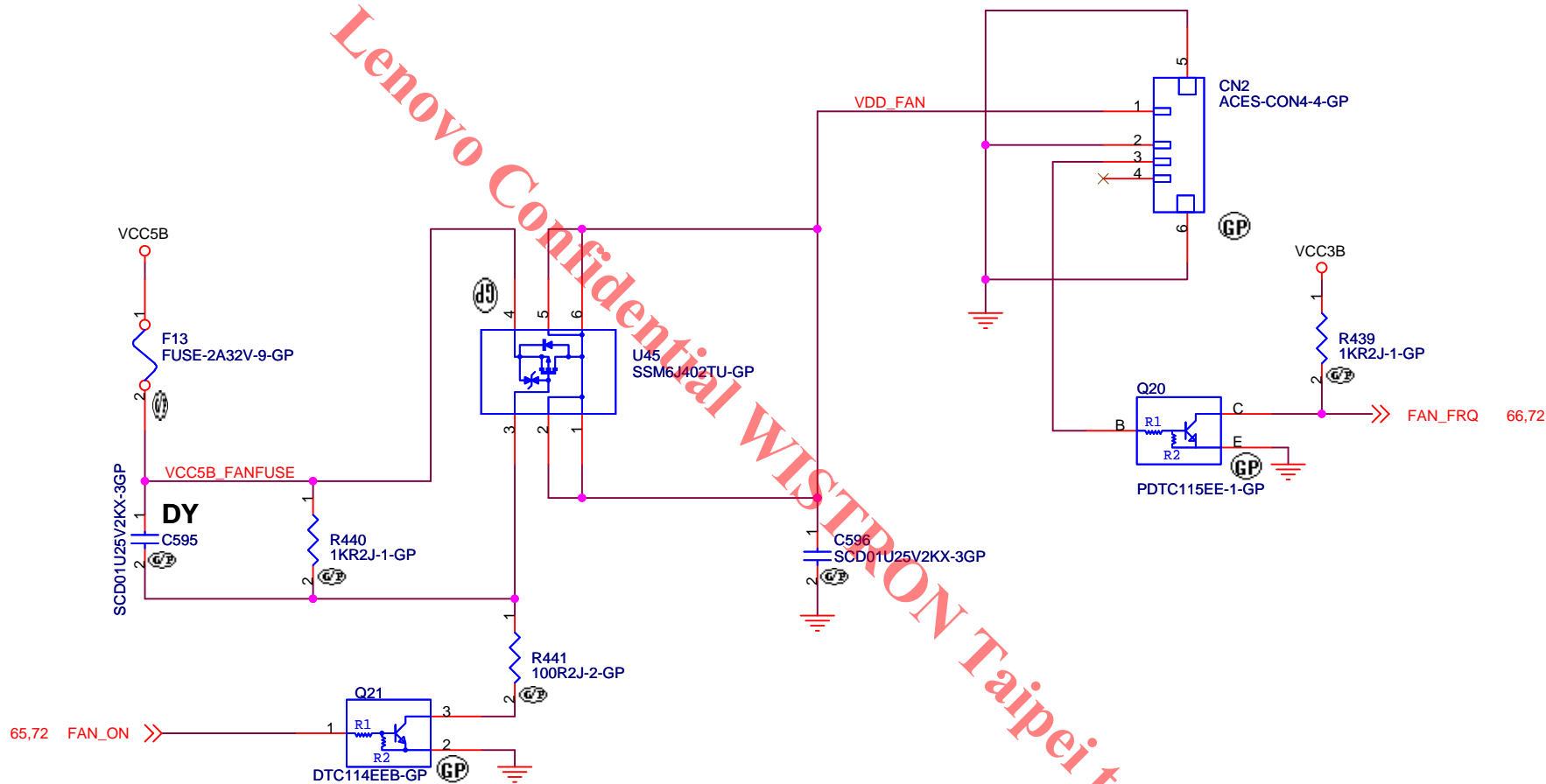




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Lenovo Confidential WISTRON Taipei to Lenovo Japan

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Title		
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Size	Document Number	Rev
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Date:	Tuesday, May 05, 2009	Sheet 69 of 109



<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**FAN CONTROL**

Size  
A4

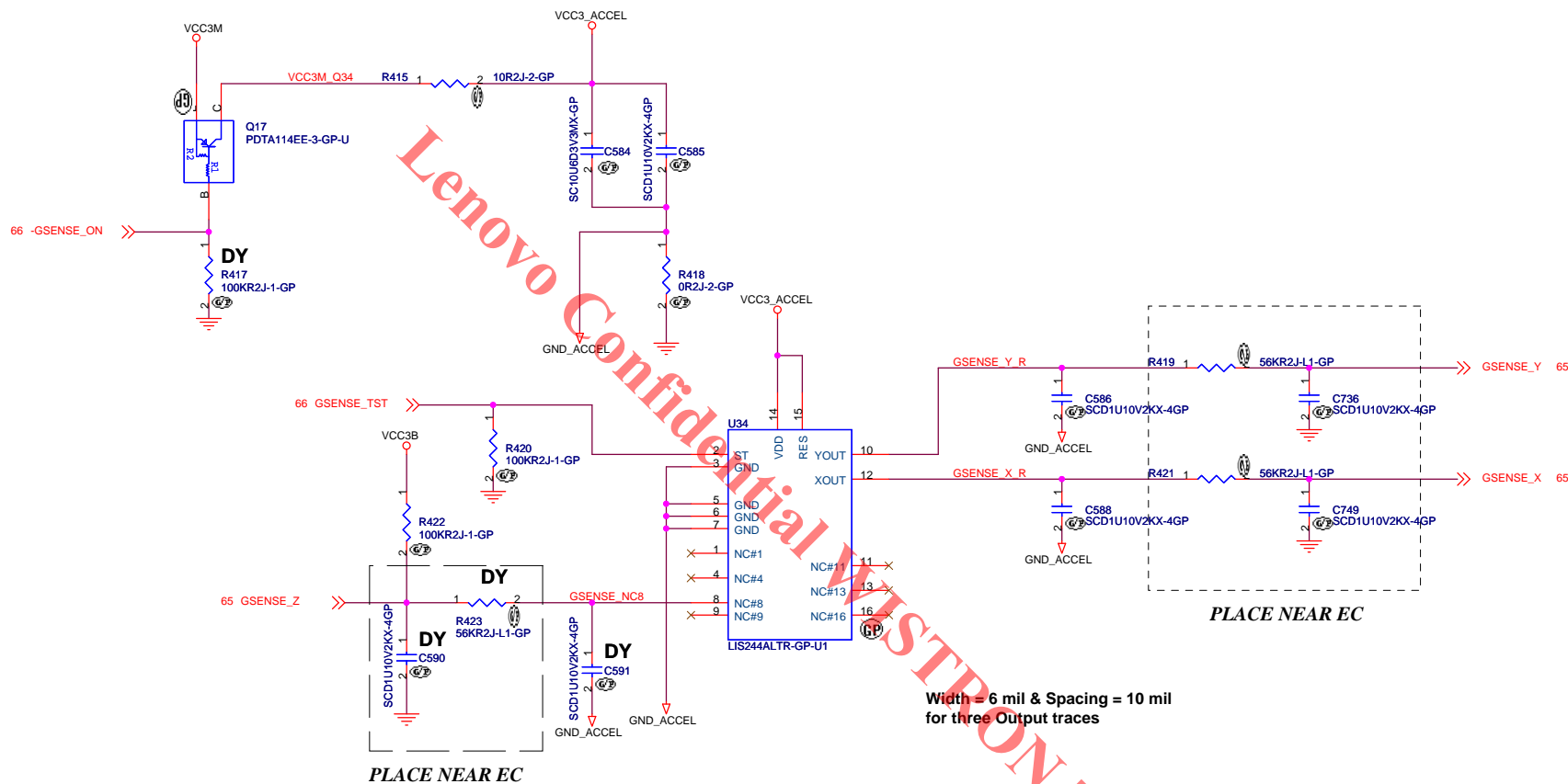
Document Number

**Kendo-1 WS**

Rev  
SC

Date: Tuesday, May 05, 2009

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PLACE NEAR EC

PLACE NEAR EC

Width = 6 mil & Spacing = 10 mil  
for three Output traces

LIS244AL LIS34AL		NO ACC.
R417	NO-ASM	ASM
R420	ASM	ASM
U34	ASM	NO-ASM
Q17	ASM	NO-ASM
R415	10-OHM	NO-ASM
C585	ASM	NO-ASM
C584	ASM	NO-ASM
C586	ASM	NO-ASM
C588	ASM	NO-ASM
R419	56K	NO-ASM
C736	ASM	NO-ASM
R421	56K	NO-ASM
C749	ASM	NO-ASM
C591	NO-ASM	NO-ASM
R423	NO-ASM	NO-ASM
C590	NO-ASM	NO-ASM
R422	ASM	ASM

#### Layout Comment :

(1) Place C586, C588, Q17, R415, R417, C584, C585, R420 close to U34.

(2) Avoid routing under DCDC switching area.

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<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**G-SENSOR**

Size  
A3

Document Number

**Kendo-1 WS**

Rev  
SC

Date: Tuesday, May 05, 2009

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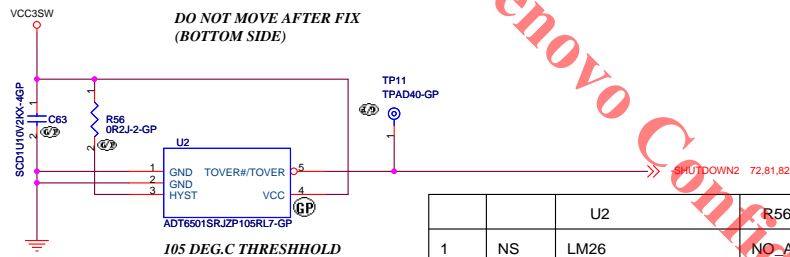


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Lenovo Confidential WISTRON Taipei to Lenovo Japan

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Title BLANK		
Size A3	Document Number Kendo-1 WS	Rev SC
Date: Tuesday, May 05, 2009		Sheet 73 of 109

## Thermal Sensor for CPU



		U2	R56
1	NS	LM26	NO_ASM
2	MAXIM	MAX6519	NO_ASM
3	ADI	ADT6501SRJZP105RL7	ASM
4	MAXIM	MAX6501UKP105+1	ASM
5	ROHM	BDE1055G	ASM

THESE CAPS MUST BE PLACED AS  
CLOSE AS POSSIBLE TO MAX6593

TO Discrete GFX

TO GBE

TO MEMORY TOP

TO WLAN

TO MEMORY BOTTOM



MAX6593TG9A+

Remote 1: Discrete GFX Address 01h.

Remote 2: GigaBitEthernet Address 02h.

Remote 3: SODIMM Top Address 03h.

Remote 4: Wireless LAN Slot Address 04h.

Remote 5: SODIMM Bottom Address 07h.

Local: BASE Cover (Under PCH) Address 05h.

H8 I2C Bus 2 ADDRESS : 4DH

TEST PAD FOR THERMAL SENSORS

Layout Comment :

- (1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
- (2) Also, it should not be overlapped with large amplitude trace line in either.

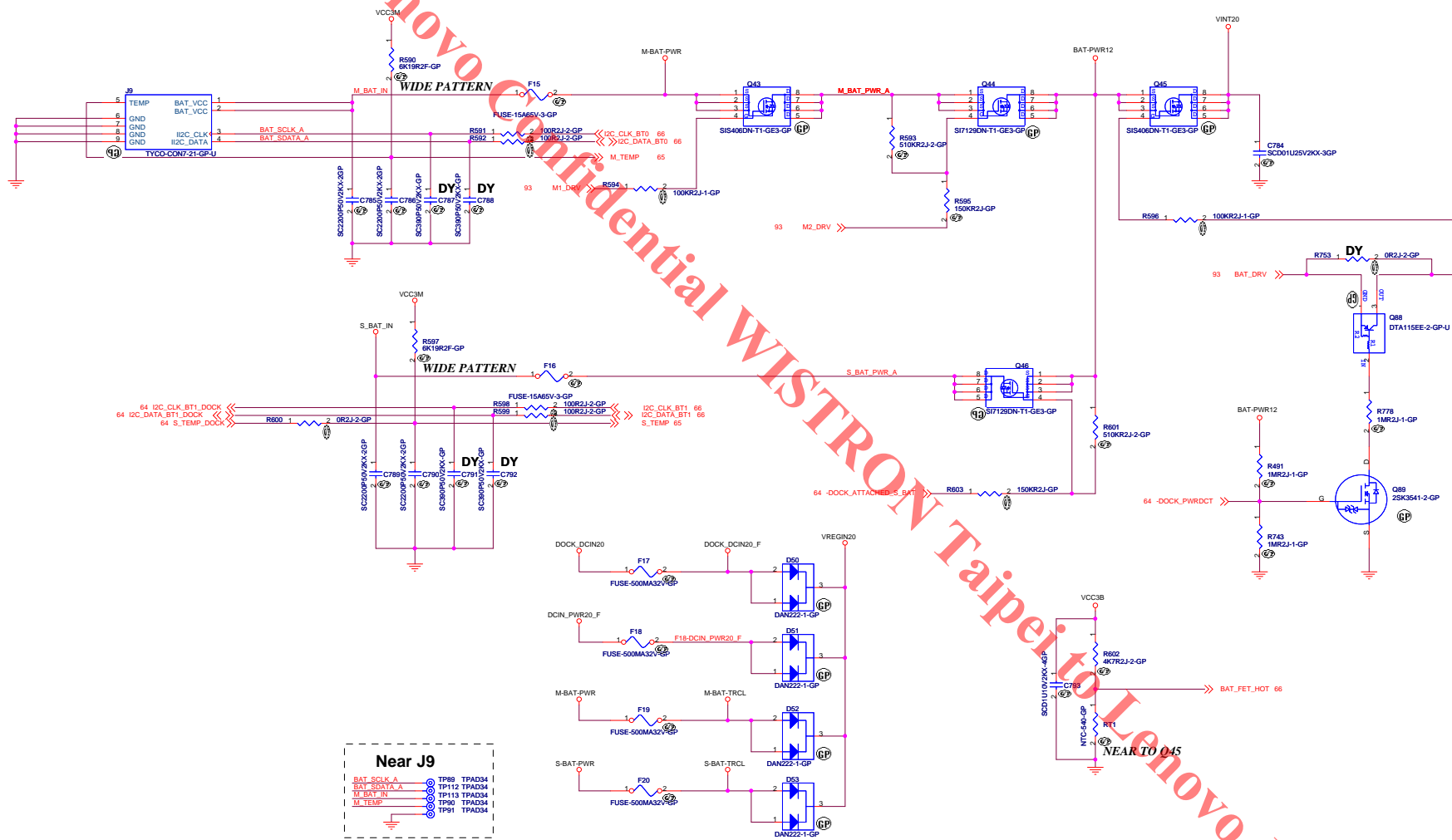
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title THERMAL SENSOR	
Size Custom	Document Number Kendo-1 WS
Date: Tuesday, May 05, 2009	Sheet 74 of 108
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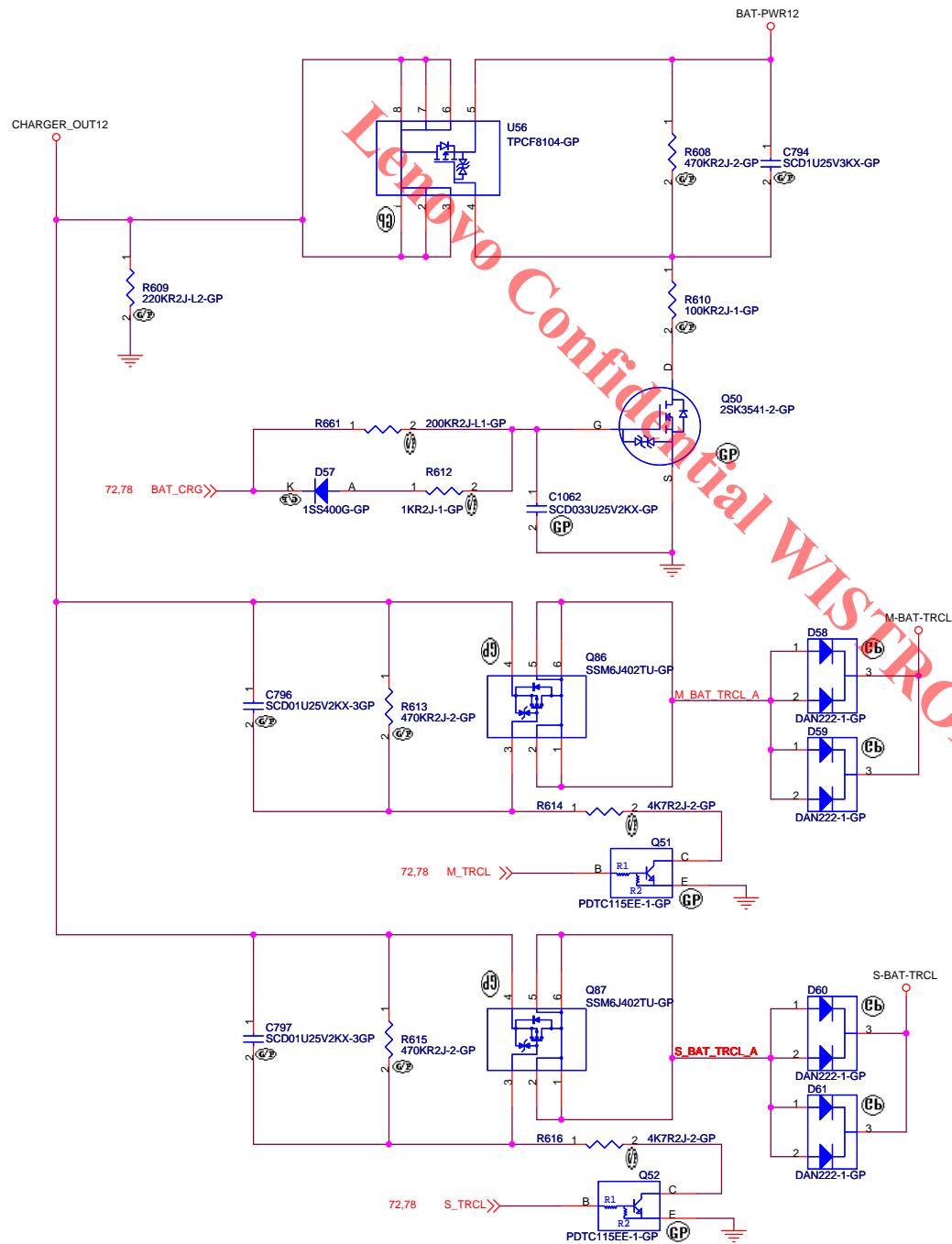


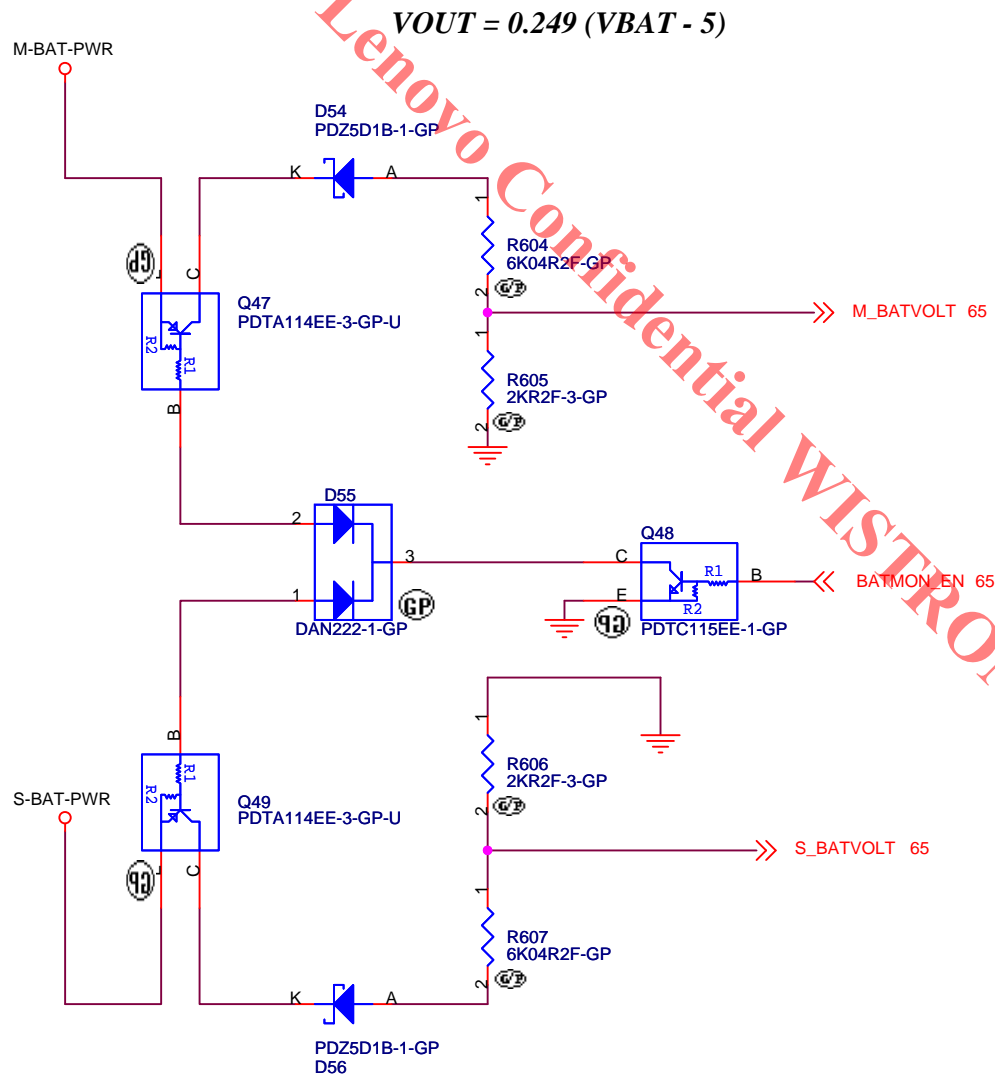












<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**BATTERY MONITOR**

Size  
A4

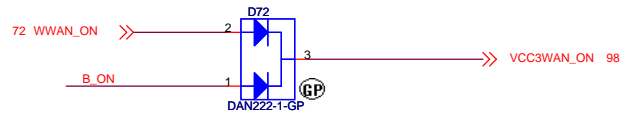
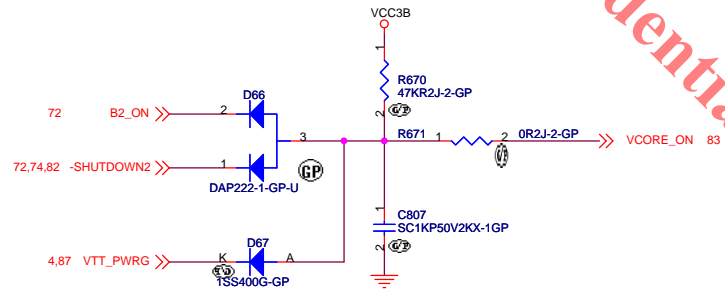
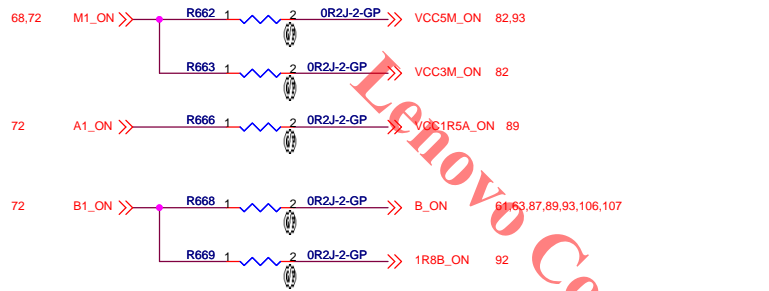
Document Number

**Kendo-1 WS**

Rev  
**SC**

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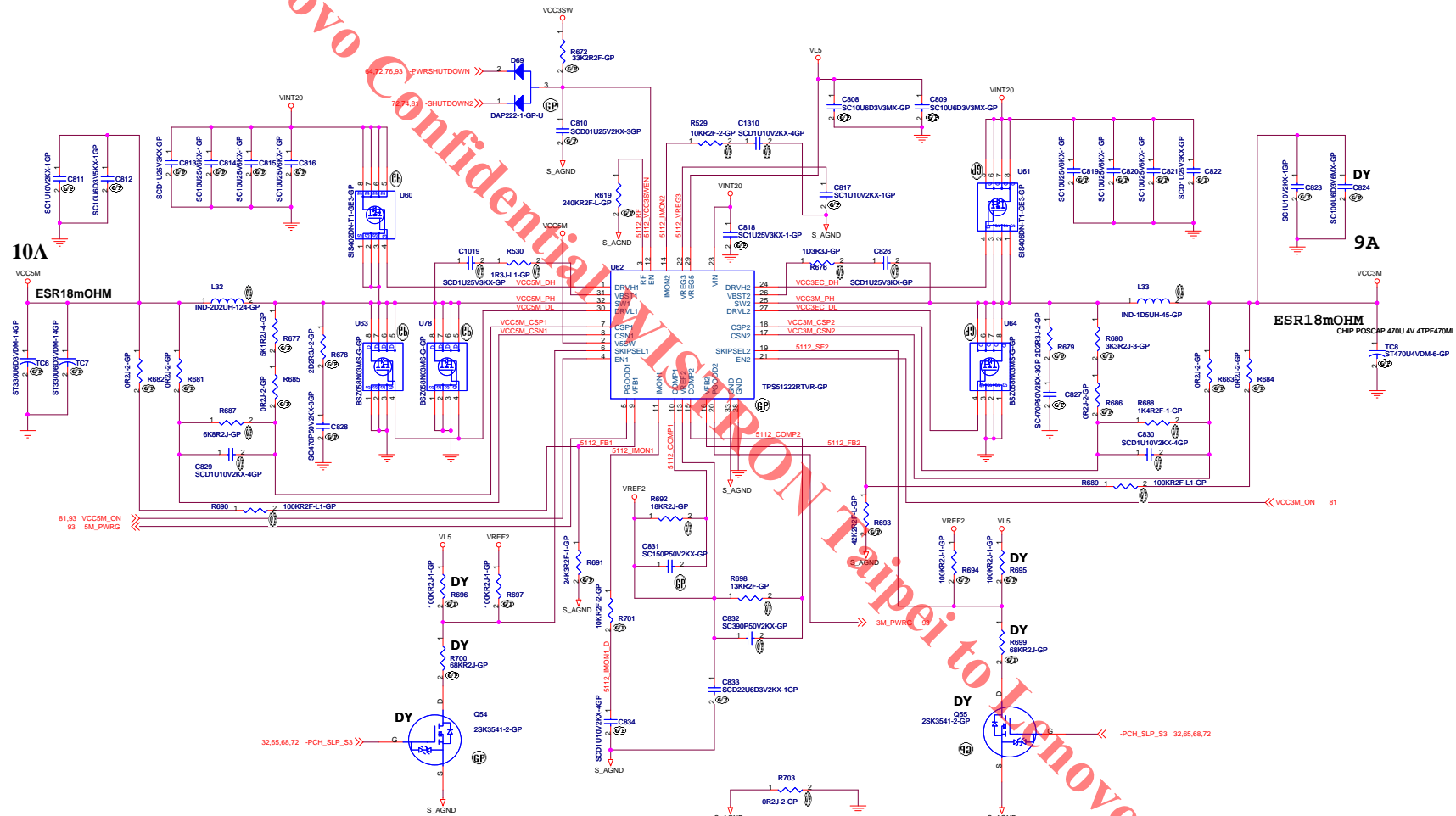
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ALWAYS ON	YES	NO
D72	ASM	NO_ASM

<Core Design>

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

POWER SEQUENCE		
Size A3	Document Number Kendo-1 WS	Rev SC
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<Core Design>

緯創資通 Wistron Corporation  
21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsinchu,  
Taipei Hsien 221, Taiwan, R.O.C.

File			
DC/DC VCC5M/VCC3M			
Size			
A2	Document Number	Kendo-1 WS	Rev SC
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Lenovo Confidential WISTRON Taipei to Lenovo Japan

<Variant Name>		
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Title		
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Size	Document Number	Rev
A3	Kendo-1 WS	SC
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Lenovo Confidential WISTRON Taipei to Lenovo Japan

<Variant Name>		
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Size A3	Document Number Kendo-1 WS	Rev SC
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Lenovo Confidential WISTRON Taipei to Lenovo Japan

<Variant Name>	
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Title BLANK	
Size A3	Document Number Kendo-1 WS
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Rev SC	

**VCC0R75B I<sub>max</sub>=1A**

VTTSENS should sense at C1021's hot terminal.

Close to output capacitor

State	S3	S5	VDDR	VTTREF	VTT
S0	Hi	Hi	On	On	On
S3	Lo	Hi	On	On	Off(Hi-Z)
S4/S5	Lo	Lo	Off	Off	Off

Core Design>

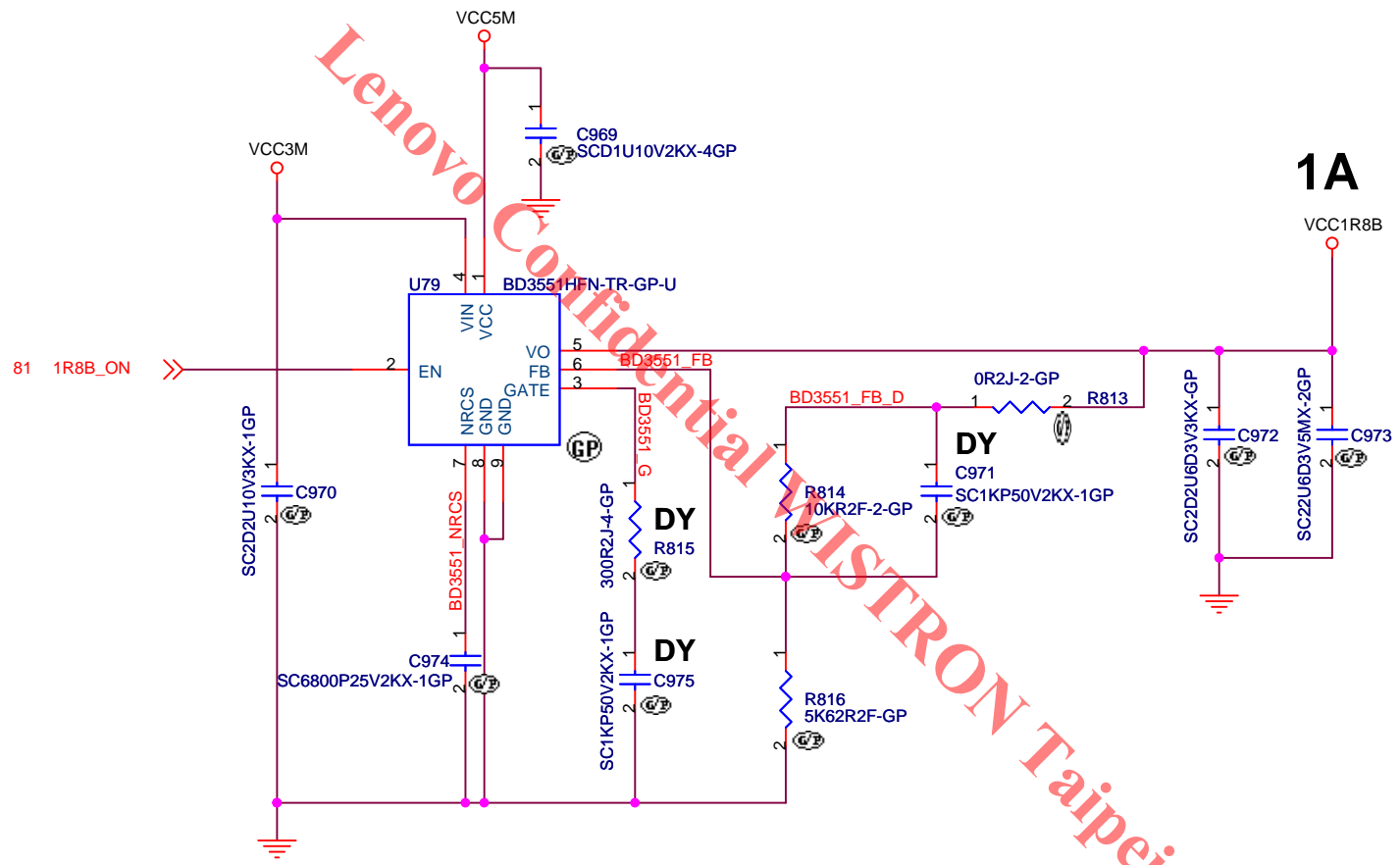
Wistron Corporation			
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title <b>DC-DC VCC0R75B</b>			
Size A3	Document Number	Kendo-1 WS	
Date: Tuesday, May 05, 2009	Sheet 89	of	109

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Lenovo Confidential WISTRON Taipei to Lenovo Japan

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Title <b>BLANK</b>		
Size A3	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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1A  
VCC1R8B

<Core Design>

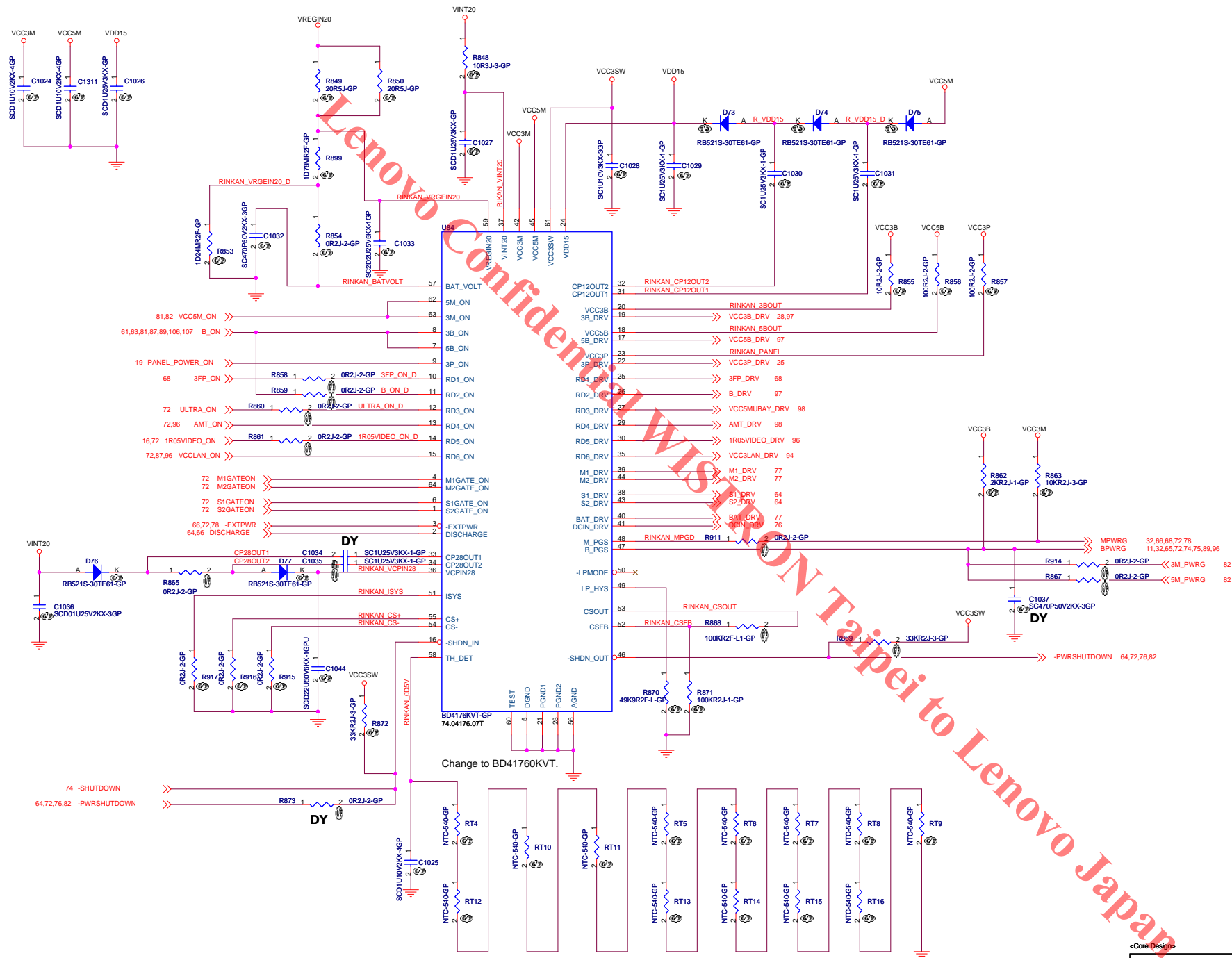
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**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			<b>DC-DC VCC1R8B</b>	
Size A4	Document Number		<b>Kendo-1 WS</b>	
Date: Tuesday, May 05, 2009		Sheet 92	of	109
		Rev		SC

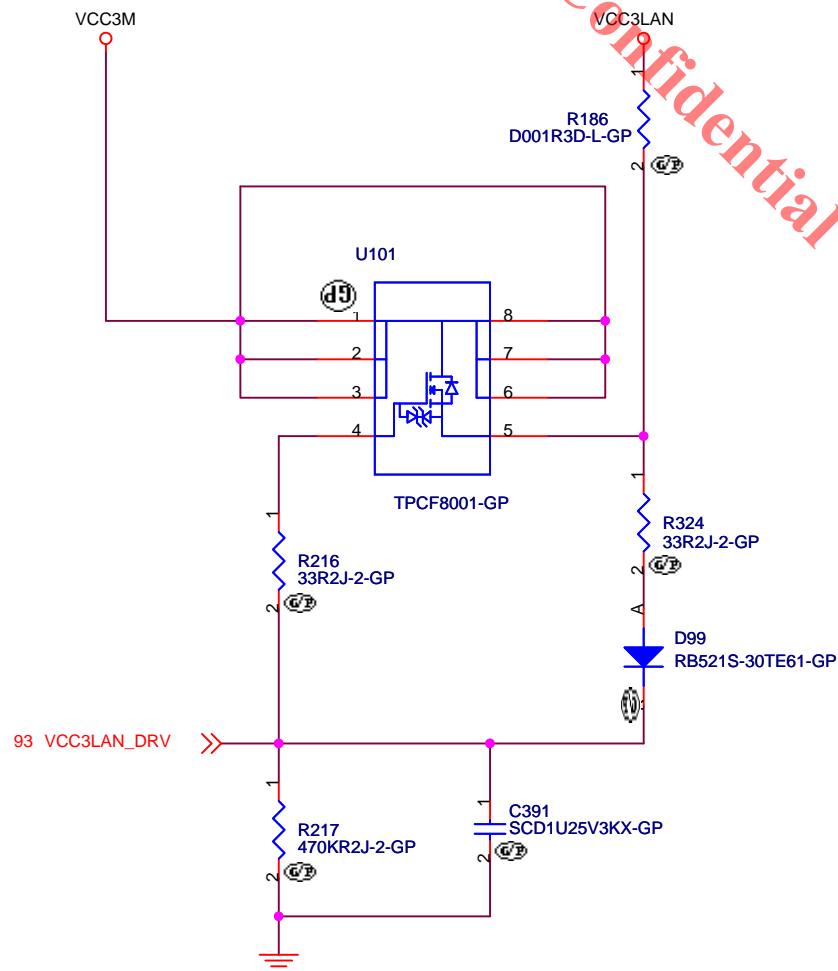
www.vinafix.vn





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DC/DC RINKAN-2			
Document Number	Kendo-1 WS		Rev
Custom			SC
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<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

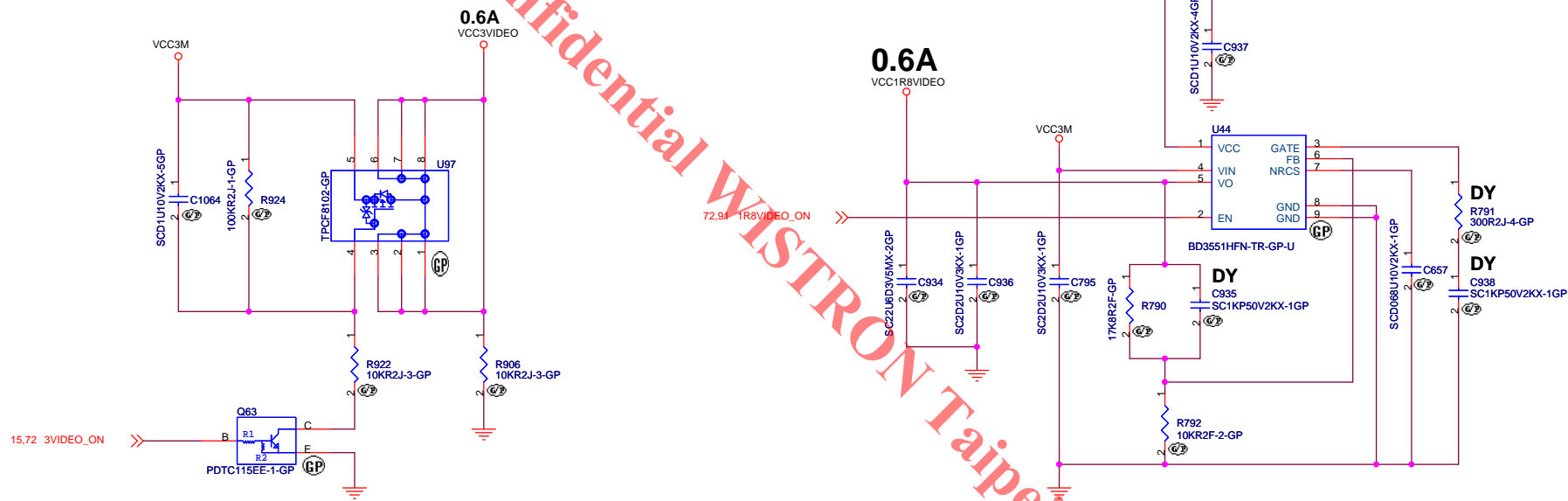
Title **LOAD SW LAN & LANPWRG**

Size A4	Document Number <b>Kendo-1 WS</b>	Rev <b>SC</b>
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Date: Tuesday, May 05, 2009 Sheet 94 of 109

www.vinafix.vn

Lenovo Confidential WISTRON Taipei to Lenovo Japan

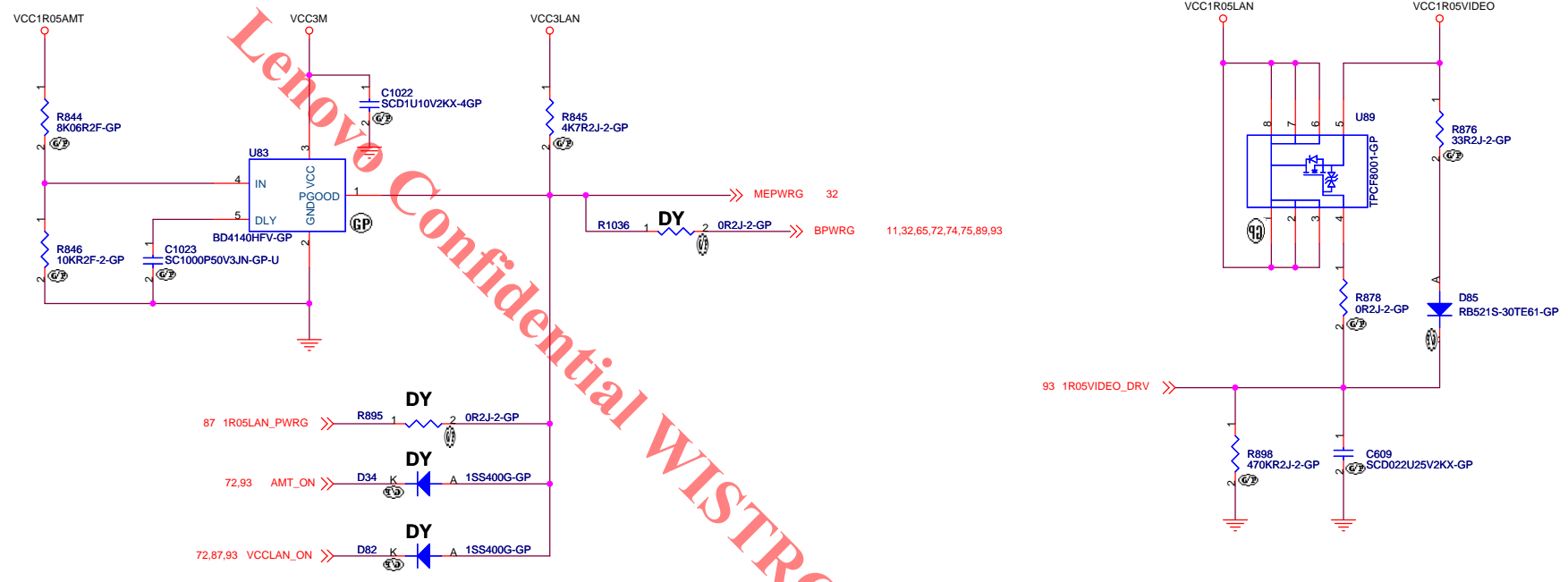


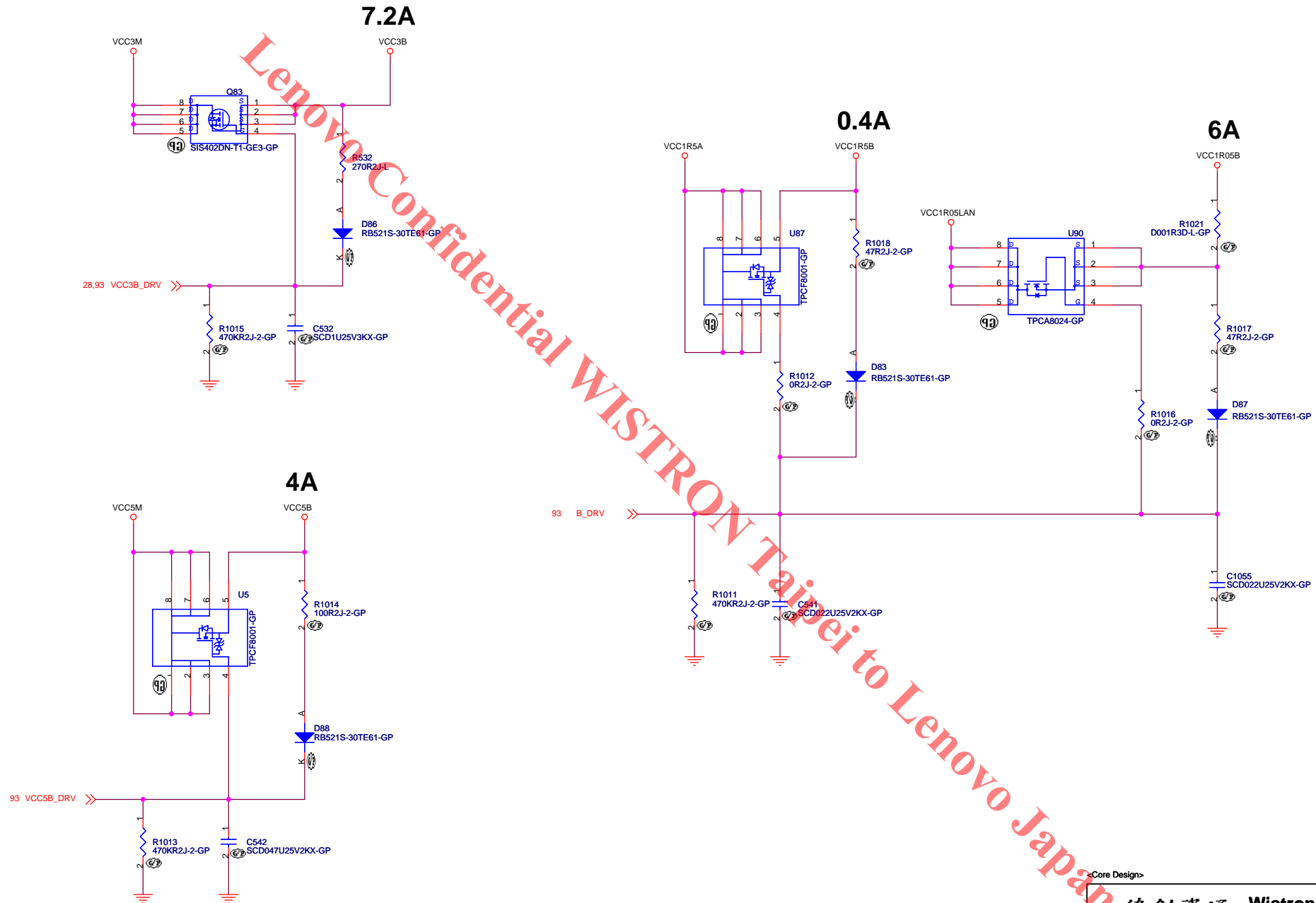
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緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title		
LOAD SW VIDEO		
Size A3	Document Number Kendo-1 WS	Rev SC
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www.vinafix.vn



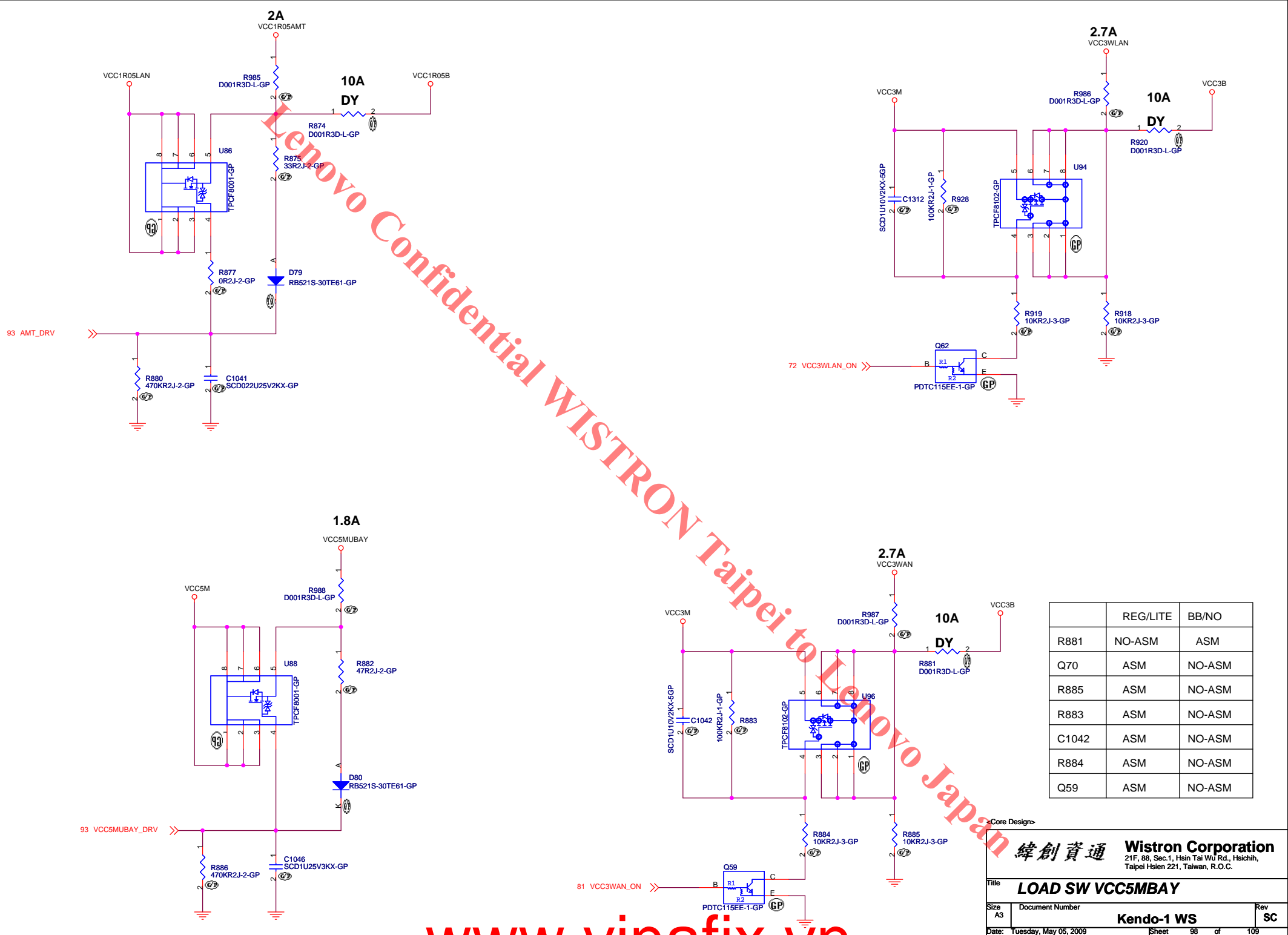


Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

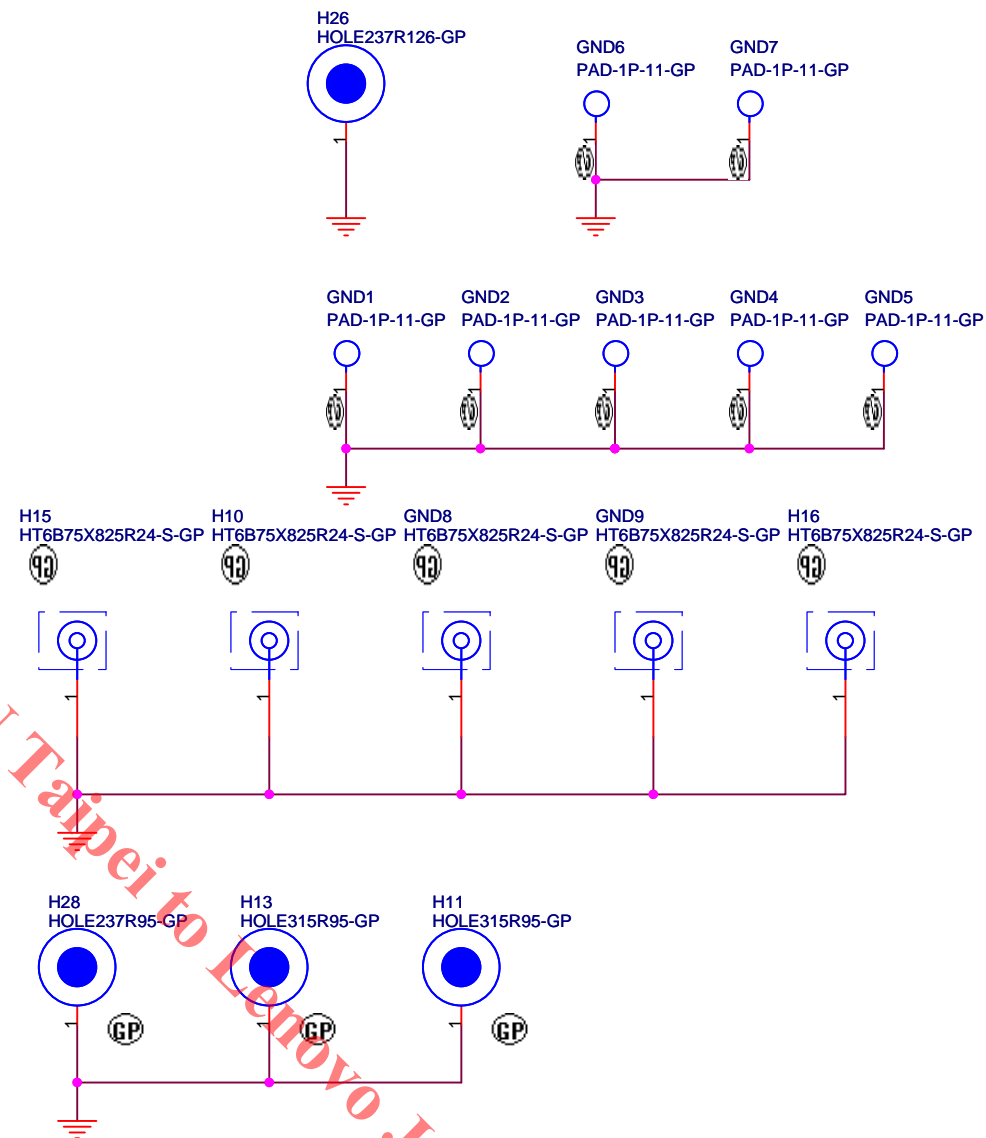
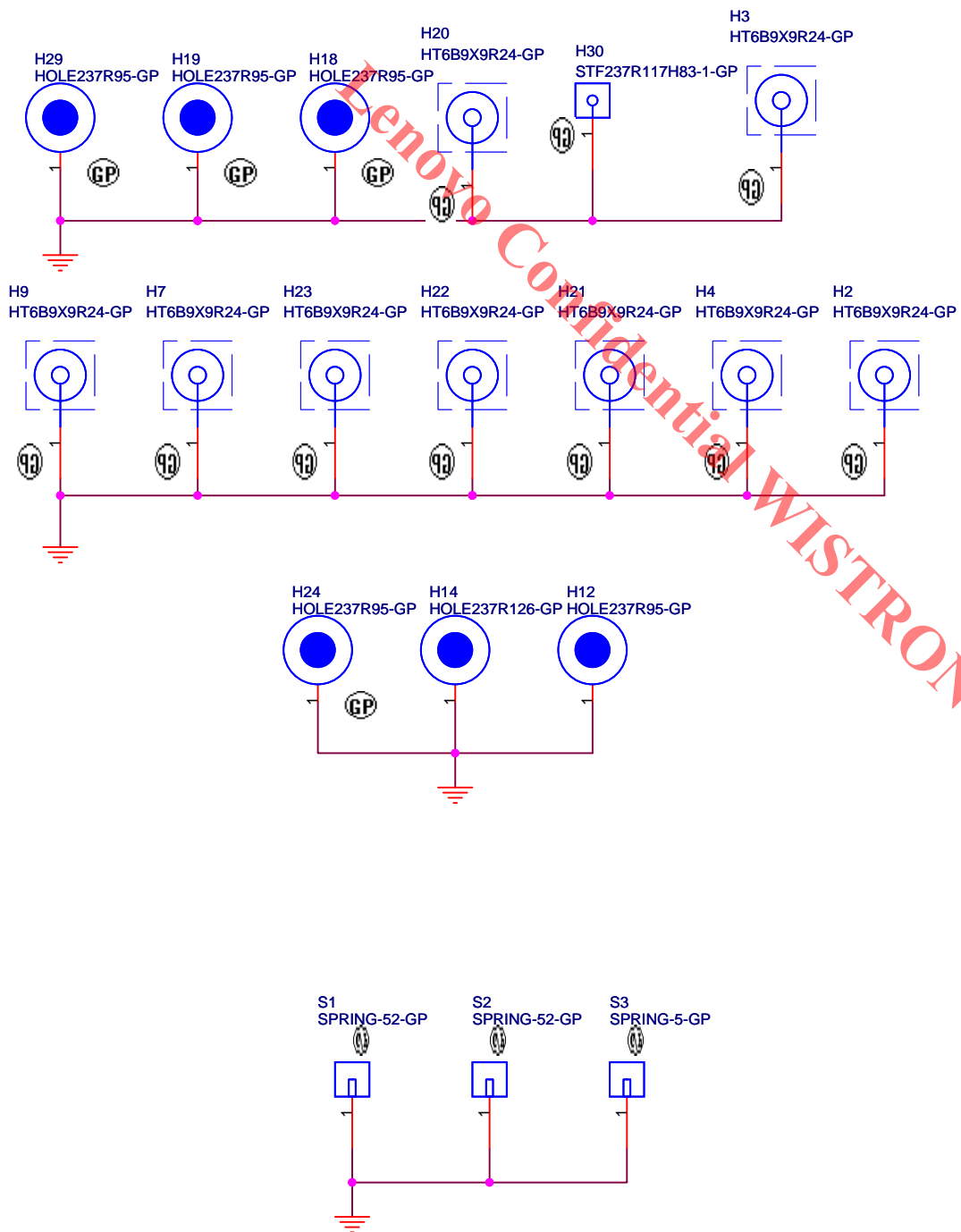
Title			
LOAD SW B			
Size A3	Document Number		Rev
	Kendo-1 WS		SC
Date:	Tuesday, May 05, 2009	Sheet	97 of 109




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Lenovo Confidential WISTRON Taipei to Lenovo Japan

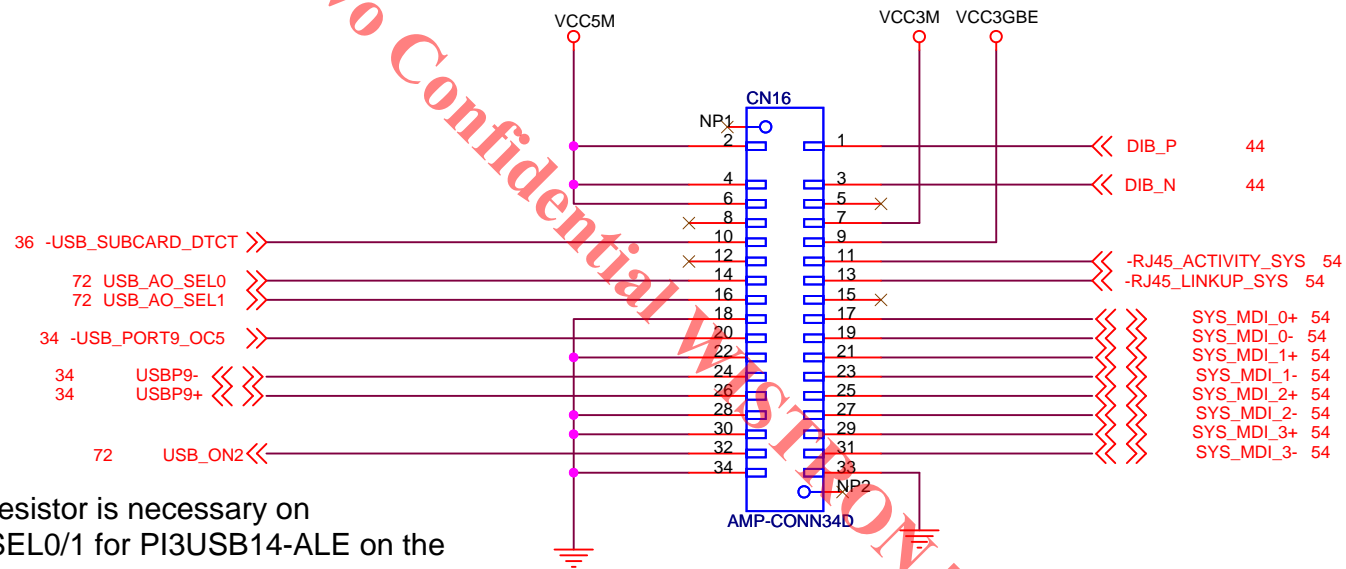
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Title		
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Size	Document Number	Rev
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<Core Design>

		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>PTH FOR SCREW HOLES</b>			
Size A4	Document Number <b>Kendo-1 WS</b>		Rev <b>SC</b>
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Pull-down resistor is necessary on  
USB\_AO\_SEL0/1 for PI3USB14-ALE on the  
USB BRD.

**Change Pin order and pin number to 34.**

<Variant Name>

緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**USB BOARD CONN**

Size  
A4

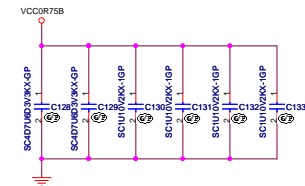
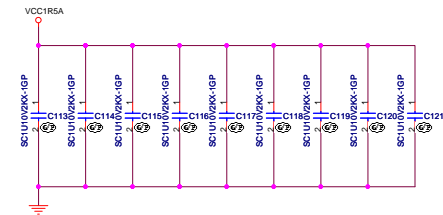
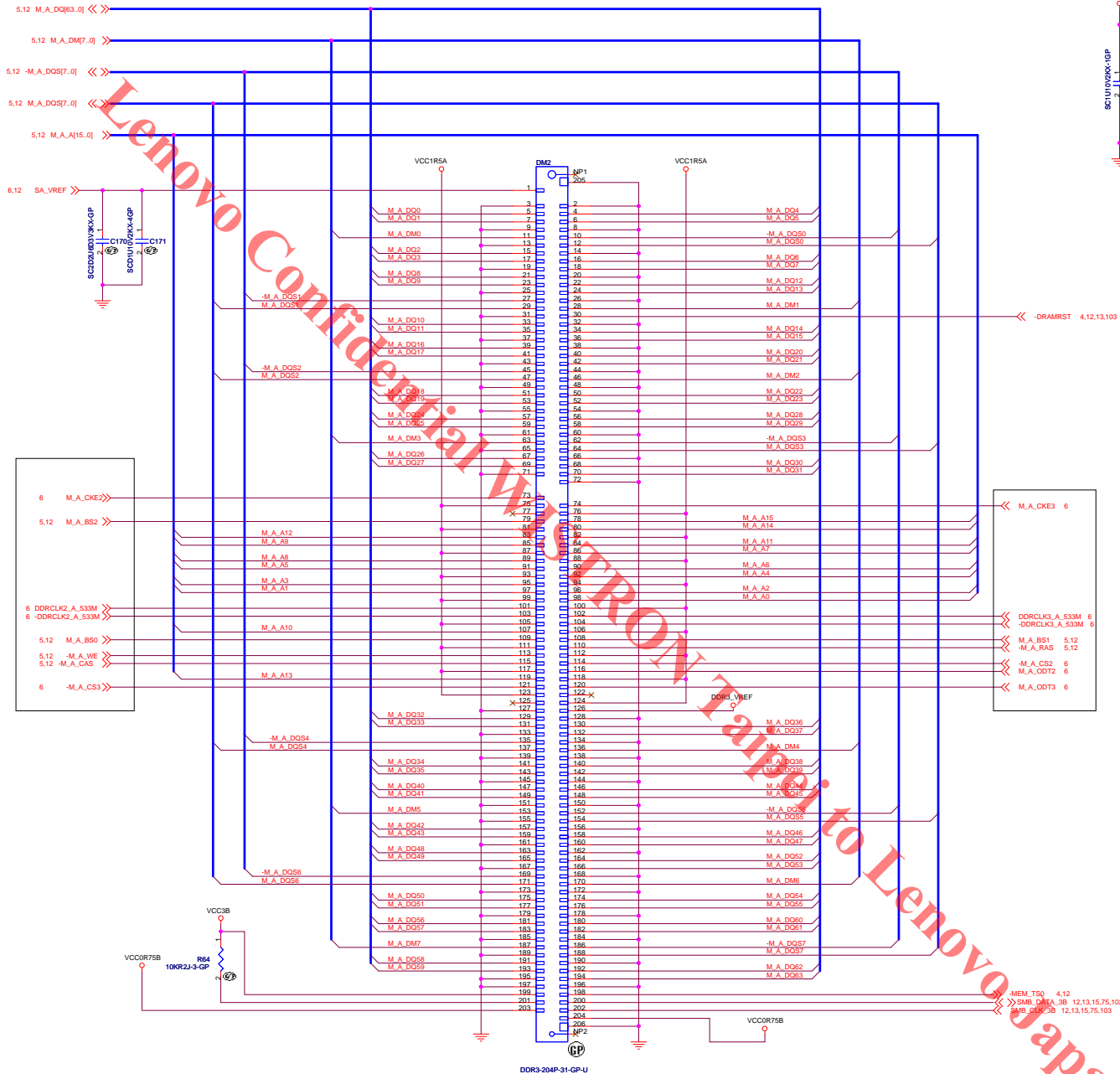
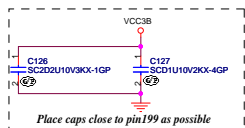
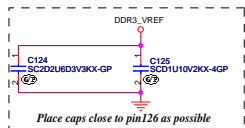
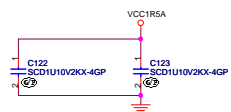
Document Number

**Kendo-1 WS**

Rev  
SC

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SPD Address : 52h

This connector should be placed on near side from CPU.

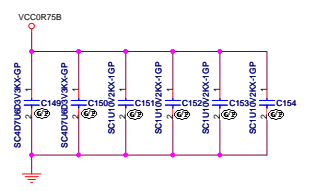
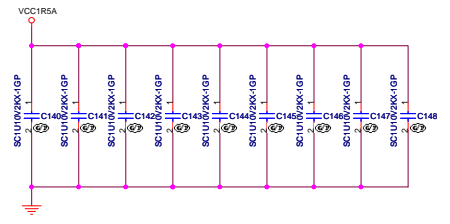
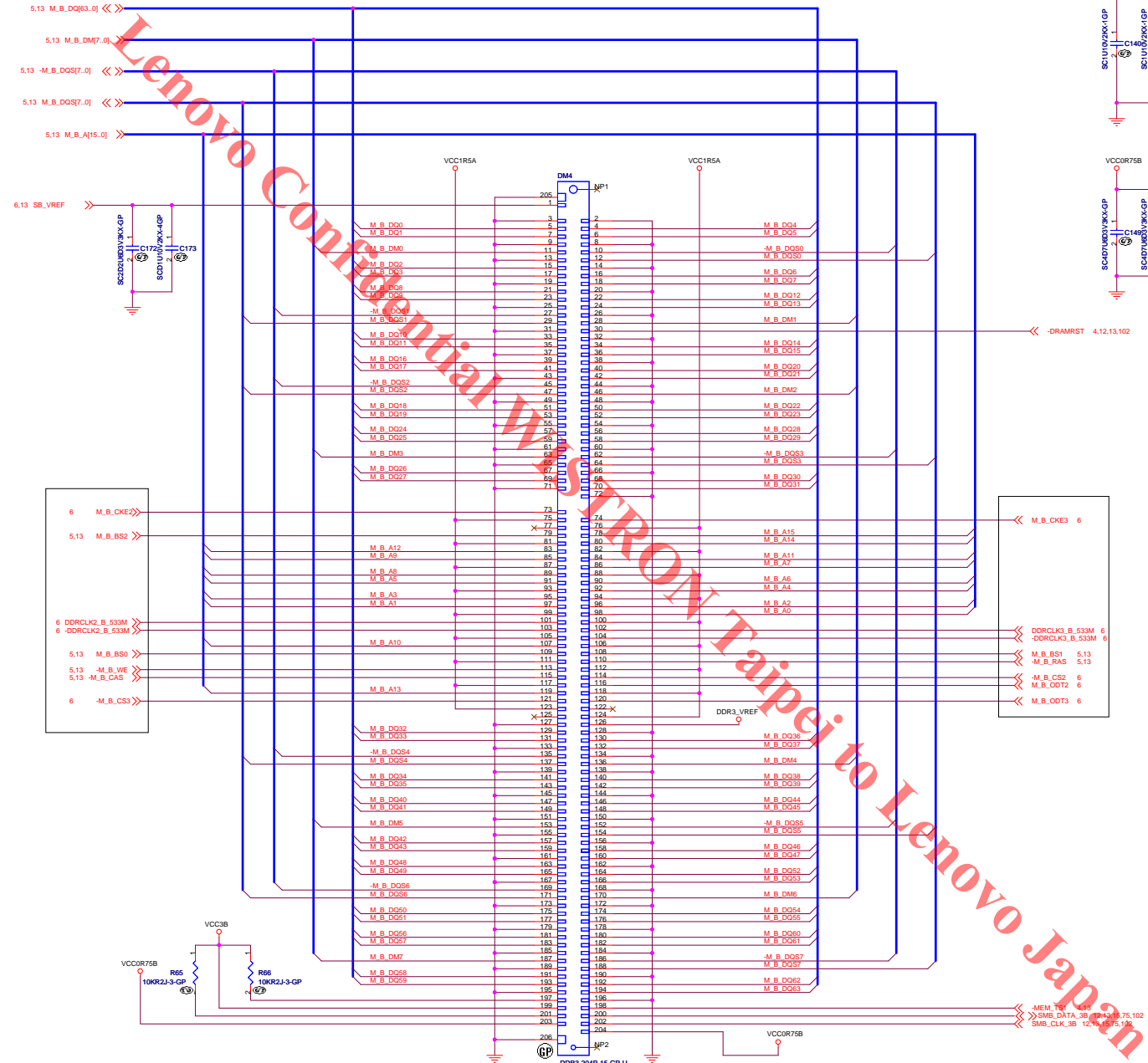
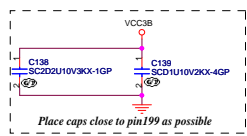
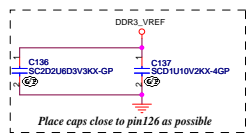
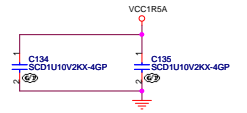
www.vinafix.vn

<Variant Name>

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

DDR3 SODIMM\_CH-A\_Secondary

Size	Document Number	Kendo-1 WS	Rev
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SPD Address : 53h  
This connector should be placed on near side from CPU.

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SODIMM should be installed on CH0 Primary at first.

From Clarksfield

SODIMM CH-A Secondary

SODIMM CH-A Primary

MA\_A\_DQ[63:0]  
M\_A\_DM[7:0]  
~M\_A\_DQS[7:0]  
M\_A\_DQS[7:0]  
M\_A\_A[15:0]  
~DRAMRST  
~M\_A\_RAS  
~M\_A\_CAS  
~M\_A\_WE  
M\_A\_BS0  
M\_A\_BS1  
M\_A\_BS2  
DDRCLK0\_A\_533M  
~DDRCLK0\_A\_533M  
DDRCLK1\_A\_533M  
~DDRCLK1\_A\_533M  
DDRCLK2\_A\_533M  
~DDRCLK2\_A\_533M  
DDRCLK3\_A\_533M  
~DDRCLK3\_A\_533M  
M\_A\_CKE0  
M\_A\_CKE1  
M\_A\_CKE2  
M\_A\_CKE3  
~M\_A\_CS0  
~M\_A\_CS1  
~M\_A\_CS2  
~M\_A\_CS3  
M\_A\_ODT0  
M\_A\_ODT1  
M\_A\_ODT2  
M\_A\_ODT3  
~MEM\_TS0  
SMB\_DATA\_3B  
SMB\_CLK\_3B

From Clarksfield

SODIMM CH-B Secondary

SODIMM CH-B Primary

MA\_B\_DQ[63:0]  
M\_B\_DM[7:0]  
~M\_B\_DQS[7:0]  
M\_B\_DQS[7:0]  
M\_B\_A[15:0]  
~DRAMRST  
~M\_B\_RAS  
~M\_B\_CAS  
~M\_B\_WE  
M\_B\_BS0  
M\_B\_BS1  
M\_B\_BS2  
DDRCLK0\_B\_533M  
~DDRCLK0\_B\_533M  
DDRCLK1\_B\_533M  
~DDRCLK1\_B\_533M  
DDRCLK2\_B\_533M  
~DDRCLK2\_B\_533M  
DDRCLK3\_B\_533M  
~DDRCLK3\_B\_533M  
M\_B\_CKE0  
M\_B\_CKE1  
M\_B\_CKE2  
M\_B\_CKE3  
~M\_B\_CS0  
~M\_B\_CS1  
~M\_B\_CS2  
~M\_B\_CS3  
M\_A\_ODT0  
M\_A\_ODT1  
M\_A\_ODT2  
M\_A\_ODT3  
~MEM\_TS1  
SMB\_DATA\_3B  
SMB\_CLK\_3B

SODIMM ITC Address :  
CH-A Primary : 50h  
CH-B Primary : 51h  
CH-A Secondary : 52h  
CH-B Secondary : 53h

Pin1 on SODIM connector (VREF\_DQ) only connects to DDR Voltage divider.  
Clarksfield H17/J17 is left.

<Variant Name>

緯創資通 Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
File	
SODIMM CONFIGURATION	
Size A2	Document Number
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FOR U12

FOR U13

FOR U14

FOR U15

UNDER THE MEMORY

CLOSE TO THE MEMORY

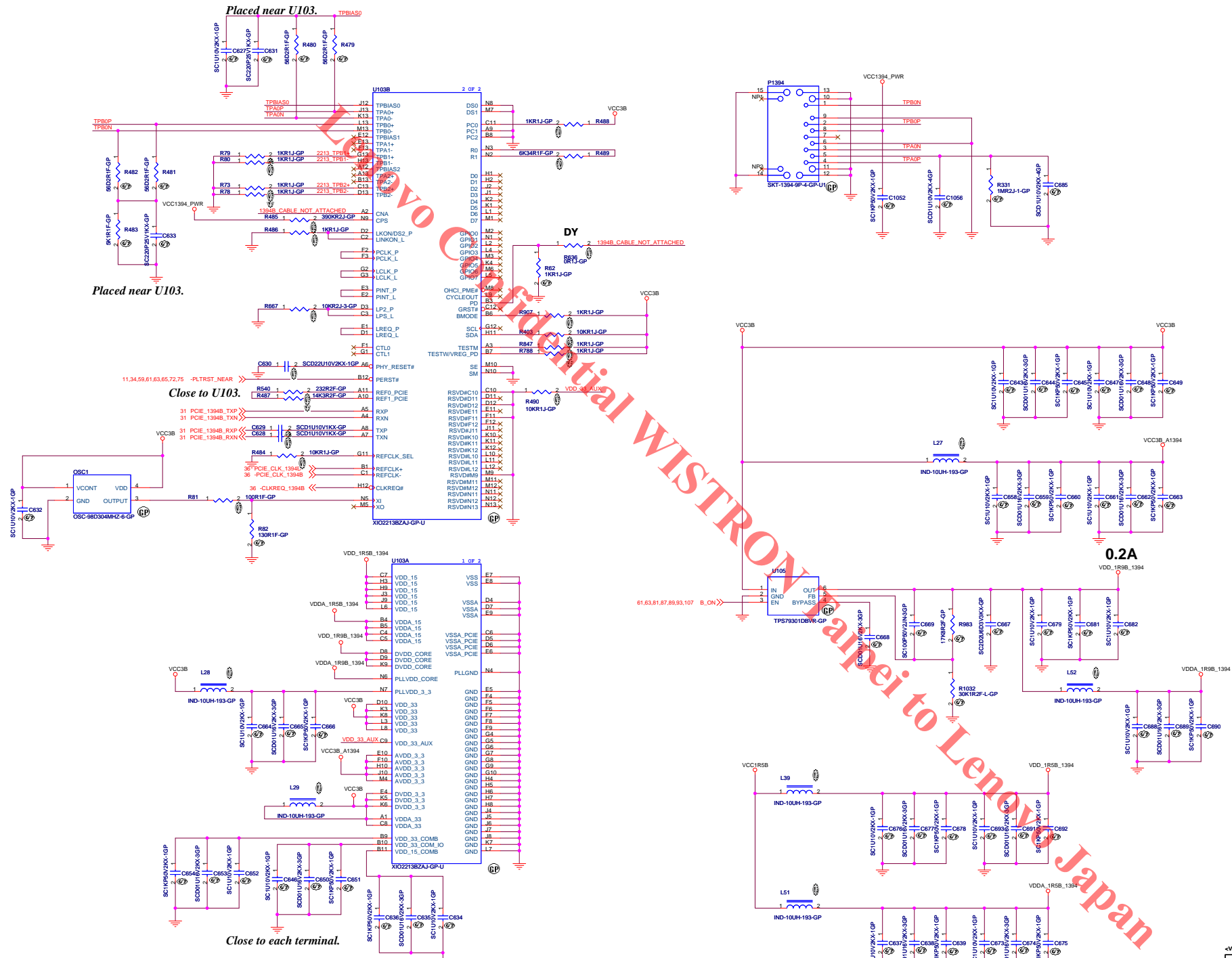
UNDER THE MEMORY

CLOSE TO THE MEMORY

VIDEO FRAME BUFFER PORT C

«Core Design»

緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.File VRAM CHANNEL-C  
Size A2 Document Number Kendo-1 WS Rev SC  
Date: Tuesday, May 05, 2009 Sheet 105 of 109



	Supplier	Vendo P/N	WISTRON P/N
1	KDS	DSO321SR 98.304M 50PPM	82.20075.061
2	HARMONY	HSO321S 98.304M 50PPM	82.20075.071

~Variant Name~

**緯創資通 Wistron Corporation**  
21F, 8th, Sec.1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsien 221, Taiwan, R.O.C.

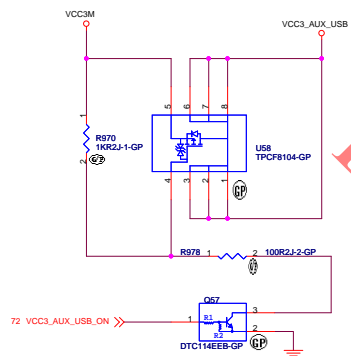
File: **1394B**

Size: A2 Document Number: **Kendo-1 WS** Rev: **SC**

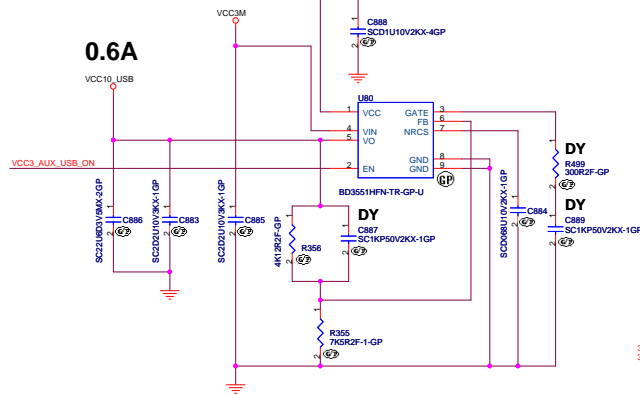
Date: Tuesday, May 05, 2009 Sheet: 108 of 109



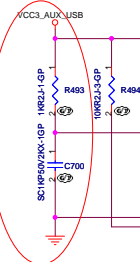
0.6A



0.6A



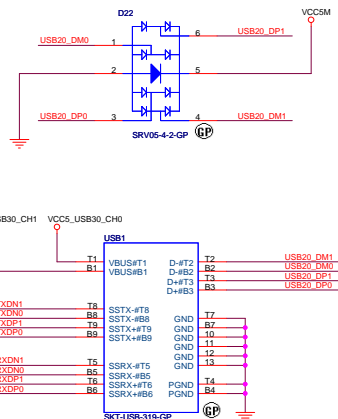
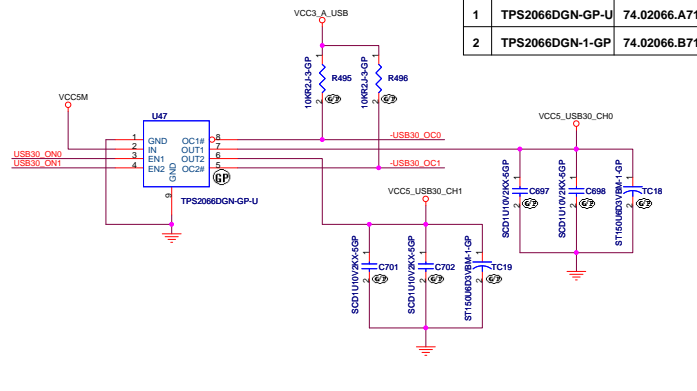
If Wakeup function from D3 cold is required,  
VCC3\_AUX should be applied from system.



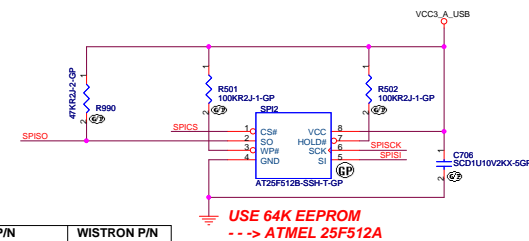
	NEC P/N	WISTRON P/N	
1	UPD720200F1-DAK-A-GP	71.72020.A0U	MP
2	UPD720200F1-DAK-SSA-A	71.72020.00U	ES

HELE Recommended Conditions:  
Normal Frequency: 24MHz.  
Frequency Tolerance: +/- 30ppm.  
Load Frequency: 12pF.  
Effective Series Resistance: 50-ohm.  
Effective Shunt Capacitance: 2pF.

	T1 P/N	WISTRON P/N
1	TPS2066DGN-GP-U	74.02066.A71
2	TPS2066DGN-1-GP	74.02066.B71

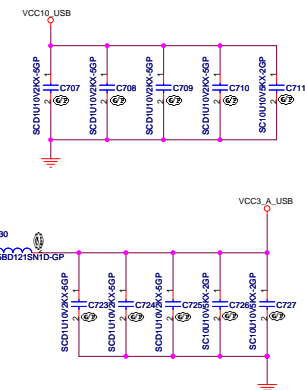


## USB3.0 CONNECTOR



	Supplier	Vendo P/N	WISTRON P/N
1	ATMEL	AT25F512B-SSH-T	72.25512.F01
2	MXIC	MX25L512MC-12G	72.25512.E01

USE 64K EEPROM  
--> ATMEL 25F512A



NOT MANDATORY TO PLACE.

www.vinafix.vn

&lt;Variant Name&gt;

緯創資通 Wistron Corporation	
21F, 8th, Sec. 1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsien 221, Taiwan, R.O.C.	
File	
USB 3.0	
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Date: Tuesday, May 05, 2009	Sheet 108 of 109



# Long power trace EMI decoupling caps

Lenovo Confidential WISTRON Taipei to Lenovo Japan

<Core Design>		
緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title		
EMI DECOUPLING		
Size	Document Number	Rev
Custom	Kendo-1 WS	SC
Date: Tuesday, May 05, 2009		Sheet 109 of 109